



60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Tc = +25°C (Note 9)	
60V	3.1mΩ @ V _G S = 10V	100A	

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- DC Motor Control
- Synchronous Rectification
- DC-DC Converters

Features

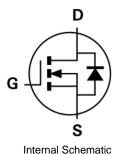
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_a Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH6004SPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

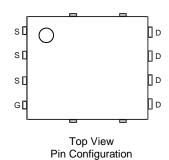
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)







Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH6004SPSQ-13	PowerDI5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



);; = Manufacturer's Marking
H6004SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 20 = 2020)
WW = Week (01 to 53)

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	60	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 5)		lo	25 21	А	
Continuous Drain Current (Notes 6 & 9) $ T_{C} = +25^{\circ}C $ $ T_{C} = +100^{\circ}C $			lo	100 100	А
Maximum Continuous Body Diode Forward Current (Notes 6 & 9)			Is	100	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	400	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			Ism	400	Α
Avalanche Current, L=0.2mH			I _{AS}	45	Α
Avalanche Energy, L=0.2mH			Eas	200	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	167	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	0.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

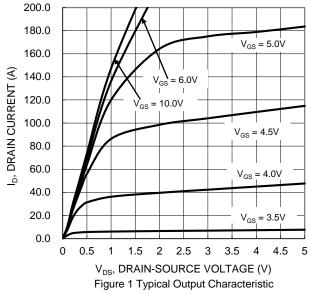
Electrical Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage		BVDSS	60	l	_	V	$V_{GS} = 0V, I_{D} = 1mA$
Zero Gate Voltage Drain Current		IDSS	-	_	1	μA	$V_{DS} = 48V$, $V_{GS} = 0V$
Zero Gate Voltage Drain Current	(Note 8)			1	100	μA	$V_{DS} = 48V$, $V_{GS} = 0V$, $T_{J} = +125^{\circ}C$
Gate-Source Leakage		Igss		1	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage		Vgs(TH)	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance		RDS(ON)		2.5	3.1	mΩ	V _G S = 10V, I _D = 50A
Diode Forward Voltage		VsD		0.9	1.2	V	$V_{GS} = 0V$, $I_{S} = 20A$
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		Ciss		4556	_	pF	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz
Output Capacitance		Coss		1383	_		
Reverse Transfer Capacitance		C _{rss}	1	105.2	_		
Gate Resistance		Rg	0.1	0.66	1.9	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge		Qg		95.4	_		\\ 20\\ I= 00A
Gate-Source Charge		Qgs		21.6	_	nC	V _{DD} = 30V, I _D = 90A, V _{GS} = 10V
Gate-Drain Charge		Q_{gd}		20.4	_		VGS = 10V
Turn-On Delay Time		td(ON)	_	13.2	_		
Turn-On Rise Time		t _R		11.7	_		$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time		tD(OFF)	_	31	_	ns	$I_D = 90A, R_G = 3.5\Omega$
Turn-Off Fall Time		t _F	_	12	_		
Body Diode Reverse Recovery Time		t _{RR}	_	50.5	_	ns	I _F = 50A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge		Q _{RR}	_	80.8	_	nC	- IF = 50A, α/αι = 100Α/μ5

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.6. Thermal resistance from junction to soldering point (on the exposed drain pad).7. Short duration pulse test used to minimize self-heating effect. Notes:

- 8. Guaranteed by design. Not subject to product testing.
- 9. Package limited.





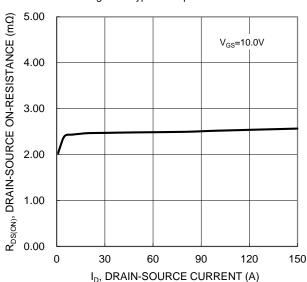


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

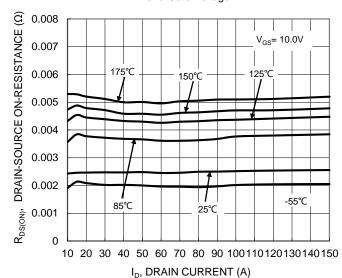


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

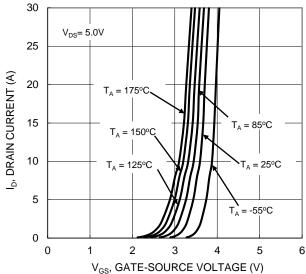


Figure 2 Typical Transfer Characteristic

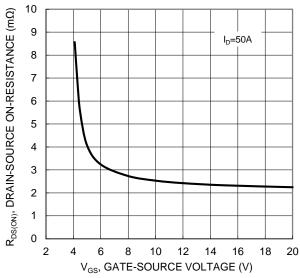


Figure 4 Typical Transfer Characteristic

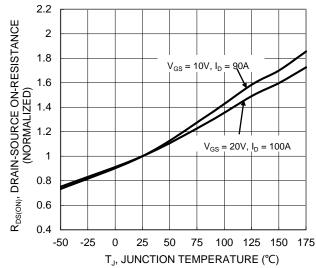


Figure 6 On-Resistance Variation with Temperature





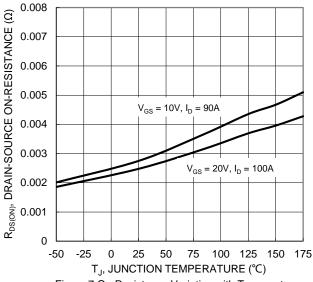
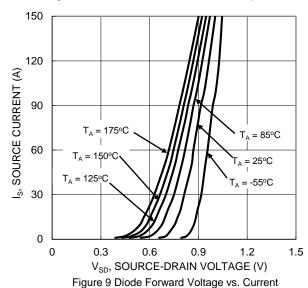


Figure 7 On-Resistance Variation with Temperature



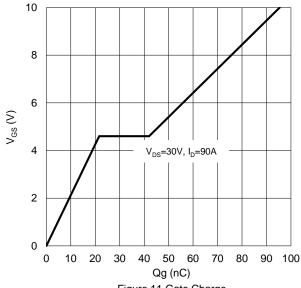


Figure 11 Gate Charge

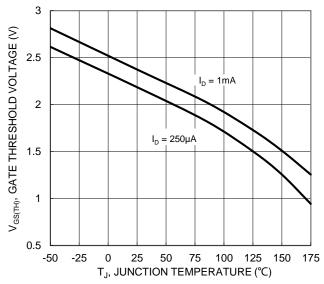
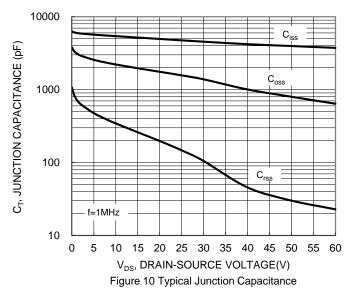


Figure 8 Gate Threshold Variation vs. Temperature



 $\begin{array}{c} \text{T}_{\text{J(Max)}} = 175\,^{\circ}\text{C} \\ \text{T}_{\text{C}} = 25\,^{\circ}\text{C} \\ \text{Single Pulse} \\ \text{DUT on Infinite Heatsink} \\ \text{V}_{\text{GS}} = 10\text{V} \\ \end{array}$

 $\rm V_{\rm DS}$, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area

1000

0.1

100



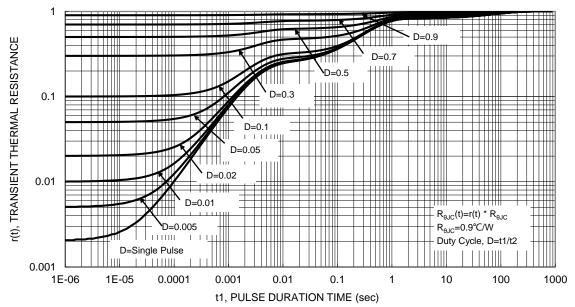


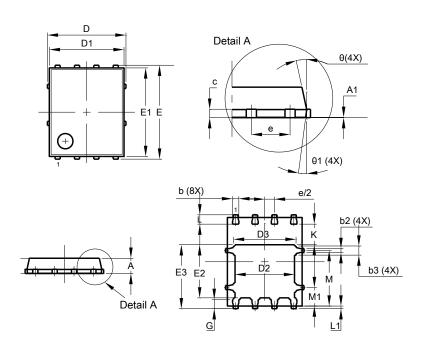
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

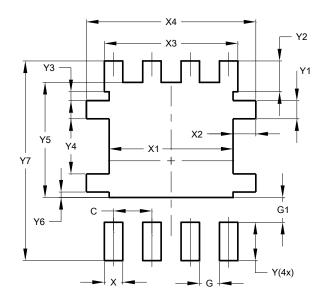


PowerDI5060-8					
Dim	Min Max		Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	ı		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D		5.15 BSC			
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90 4.30 4		4.10		
Е	(6.15 BSC			
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	1	1		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12º	11º		
Θ1	6°	8º	7º		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)				
С	1.270				
G	0.660				
G1	0.820				
X	0.610				
X1	4.100				
X2	0.755				
Х3	4.420				
X4	5.610				
Y	1.270				
Y1	0.600				
Y2	1.020				
Y3	0.295				
Y4	1.825				
Y5	3.810				
Y6	0.180				
Y7	6.610				



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