



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
20V	200mΩ @ V _{GS} = 4.5V	1.2A
200	280mΩ @ V _{GS} = 2.5V	1.1A

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- Load Switch
- Power Management Functions

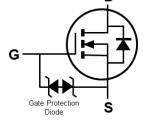
Mechanical Data

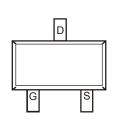
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)





SOT523





Top View

Equivalent Circuit Top View

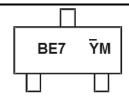
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2310UTQ-7	SOT523	3000/Tape & Reel
DMN2310UTQ-13	SOT523	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BE7 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: H = 2020)
M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н	ı	J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	20	V
Gate-Source Voltage		V_{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	I _D	1.2 1.0	А
Maximum Continuous Body Diode Forward Cur	rent (Note 6)	Is	0.6	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle :	= 1%)	I _{DM}	4.2	Α

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P_{D}	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ hetaJA}$	435	°C/W
Total Power Dissipation (Note 6)		P_{D}	0.49	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ hetaJA}$	253	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to 150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1.0	μΑ	V _{DS} = 20V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_		10	μA	V_{GS} = ±8V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	0.45	l	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	174	200		$V_{GS} = 4.5V, I_D = 300mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	211	280	mΩ	$V_{GS} = 2.5V, I_D = 250mA$	
		_	263	380		$V_{GS} = 1.8V, I_D = 100mA$	
Diode Forward Voltage	V_{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	38	_	pF	\(\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output Capacitance	Coss	_	10	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	6	_	pF	1 – 1.001112	
Gate Resistance	R_g	_	1.42	_	kΩ	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Q_g	_	0.7	_	nC	\\	
Gate-Source Charge	Q_{gs}	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	-I _D = 1A	
Turn-On Delay Time	t _{D(ON)}	_	8	_	ns		
Turn-On Rise Time	t _R	_	138	_	ns	V _{DD} = 10V, V _{GS} = 5V,	
Turn-Off Delay Time	t _{D(OFF)}	_	154	_	ns	$R_L = 1.7\Omega$, $R_G = 6\Omega$	
Turn-Off Fall Time	t _F	_	180	_	ns		

Notes:

- 5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



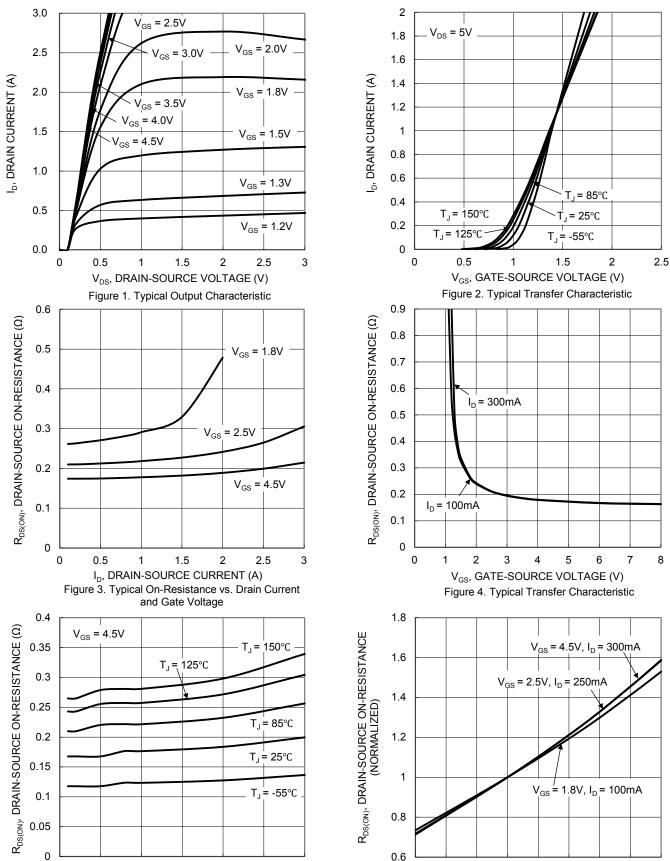


Figure 6. On-Resistance Variation with Junction Temperature

50

T_J, JUNCTION TEMPERATURE (°C)

75

0

-50

25

0.5

2

1.5

I_D, DRAIN CURRENT (A) Figure 5. Typical On-Resistance vs. Drain Current

and Junction Temperature

0

3

2.5

100

125



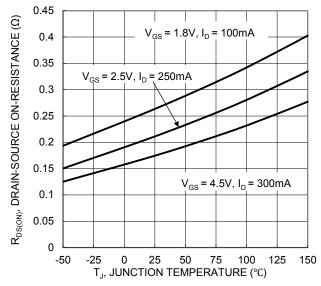


Figure 7. On-Resistance Variation with Junction Temperature

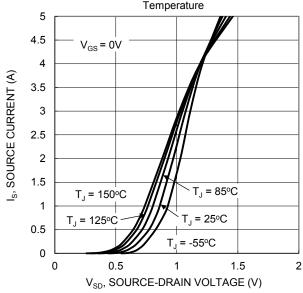


Figure 9. Diode Forward Voltage vs. Current

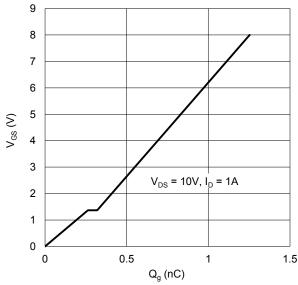


Figure 11. Gate Charge

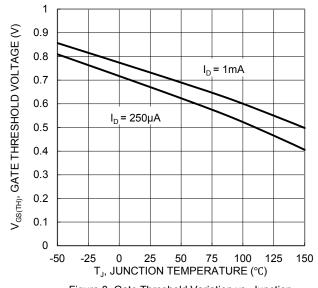


Figure 8. Gate Threshold Variation vs. Junction Temperature

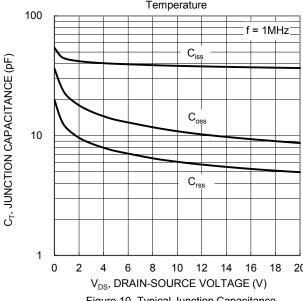


Figure 10. Typical Junction Capacitance

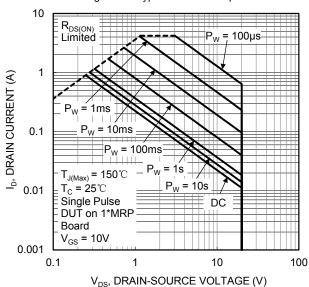


Figure 12. SOA, Safe Operation Area



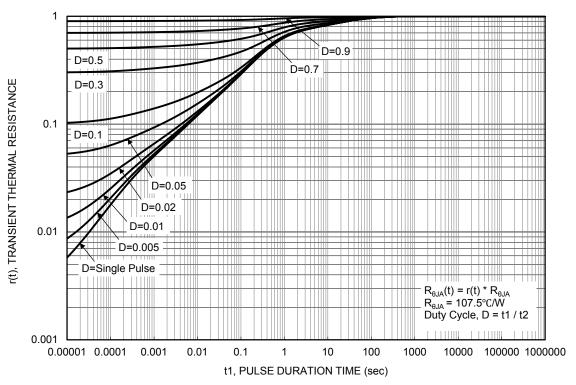
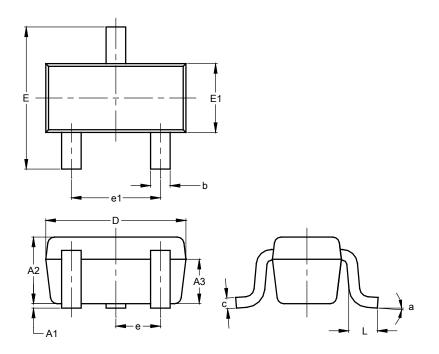


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

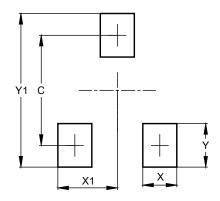
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523							
Dim	Min Max Typ						
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е	e 0.50 BSC						
e1	0.90	1.10	1.00				
٦	0.20	0.40	0.33				
а	0°		8°				
Α	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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