

INAx290 2.7V 至 120V、1.1MHz 超精密电流感应放大器

1 特性

- 宽共模电压：
 - 工作电压：2.7V 至 120V
 - 可承受电压：-20V 至 +122V
- 出色的共模抑制比 (CMRR)：
 - 160dB 直流
 - 85dB 交流 (50kHz 时)
- 精度
 - 增益：
 - 增益误差：±0.1% (最大值)
 - 增益漂移：±5ppm/°C (最大值)
 - 失调电压：
 - 失调电压：±12μV (最大值)
 - 温漂±0.2μV/°C (最大值)
- 可用增益：
 - INAx290A1：20V/V
 - INAx290A2：50V/V
 - INAx290A3：100V/V
 - INAx290A4：200V/V
 - INAx290A5：500V/V
- 高带宽：1.1MHz
- 压摆率：2V/μs
- 静态电流：370μA{23} (每通道) {24}

2 应用

- 有源天线系统 mMIMO (AAS)
- 宏远程无线电单元 (RRU)
- 48V 机架式服务器
- 48V 商用网络和服务电源
- 测试和测量

3 说明

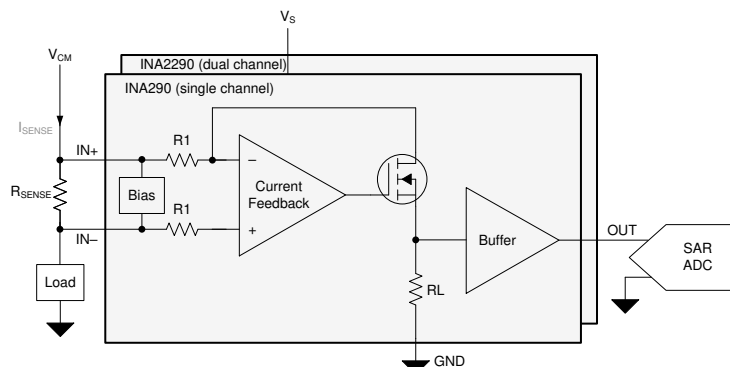
INAx290 是一款超精密电流感应放大器，可在 2.7V 至 120V 的宽共模范围内测量分流电阻器上的压降。它采用节省空间的 SC-70 封装，PCB 的空间占用仅为 2.0mm × 2.1mm。该器件在 ±12μV (最大值) 的超低失调电压、±0.1% (最大值) 的小增益误差和 160dB (典型值) 的高直流 CMRR 等特性的综合作用下，可实现超高精度的电流测量精度。INAx290 凭借 1.1MHz (增益为 20V/V) 的高带宽和 85dB (50kHz 时) 的交流 CMRR，不仅适用于直流电流测量，还适用于高速应用 (例如，快速过流保护)。

INAx290 通过在 2.7V 至 120V 的宽共模范围内测量分流电阻器上的压降，可提供进行超精密电流测量的功能。INAx290 器件采用十分节省空间的封装。单通道 INAx290 器件采用 SC-70 封装，而双通道 INA2290 器件采用 VSSOP-8 封装。

器件信息

器件型号	封装 ⁽¹⁾	封装尺寸 (标称值)
INA290	SC-70 (5)	2.00mm × 1.25mm
INA2290 ⁽²⁾	VSSOP (8)	3.00mm × 3.00mm

- (1) 如需了解所有可用封装，请参阅数据表末尾的封装选项附录。
(2) 高级信息



典型应用



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4 Revision History

Changes from Revision * (June 2020) to Revision A (August 2020)	Page
• 将数据表状态从“生产数据”更改为“混合生产”.....	1
• 在文档中添加了 INA2290 高级信息.....	1

5 说明 (续)

该器件在 $\pm 12\mu\text{V}$ (最大值) 的超低失调电压、 $\pm 0.15\%$ (最大值) 的小增益误差和 160dB (典型值) 的高直流 CMRR 等特性的综合作用下, 可实现超高精度的电流测量。INAx290 凭借 1.1MHz (增益为 20V/V) 的高带宽和 85dB (50kHz 时) 的交流 CMRR, 不仅适用于直流电流测量, 还适用于高速应用 (例如, 快速过流保护)。

INAx290 由一个 2.7V 至 20V 的单电源供电, 单通道器件仅消耗 $370\mu\text{A}$ 的电源电流 (典型值)。这些器件具有五个增益选项: 20V/V、50V/V、100V/V、200V/V 和 500V/V。借助零漂移架构的低失调电压, 可在扩展工作温度范围 (-40°C 至 125°C) 内使用低欧姆值分流器进行电流感应。

6 Pin Configuration and Functions

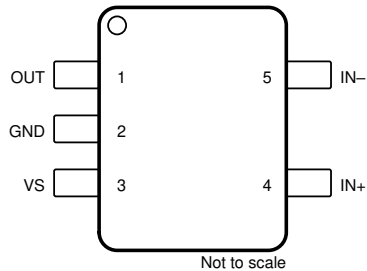
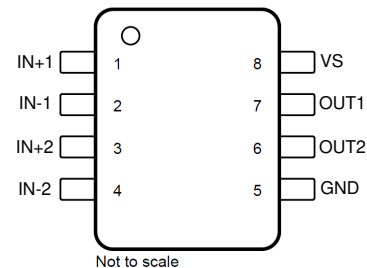


图 6-1. DCK Package 5-Pin SC-70 Top View



A. Advanced information only

图 6-2. INA2290: DGK Package 8-Pin VSSOP Top View

Pin Functions (Single channel device)

PIN		TYPE	DESCRIPTION
NAME	NO.		
GND	2	Ground	Ground
IN -	5	Input	Connect to load side of shunt resistor
IN+	4	Input	Connect to supply side of shunt resistor
OUT	1	Output	Output voltage
VS	3	Power	Power supply

Pin Functions: INA2290 (Dual Channel)

PIN		TYPE	DESCRIPTION
NAME	NO.		
GND	5	Ground	Ground
IN - 1	2	Analog input	Current-sense amplifier negative input for channel 1. Connect to load side of channel-1 sense resistor.
IN+1	1	Analog input	Current-sense amplifier positive input for channel 1. Connect to bus-voltage side of channel-1 sense resistor.
IN - 2	4	Analog input	Current-sense amplifier negative input for channel 2. Connect to load side of channel-2 sense resistor.
IN+2	3	Analog input	Current-sense amplifier positive input for channel 2. Connect to bus-voltage side of channel-2 sense resistor.
OUT1	7	Analog output	Channel 1 output voltage
OUT2	6	Analog output	Channel 2 output voltage
VS	8	Power	Power supply

7 Specifications

7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

		MIN	MAX	UNIT
Supply Voltage (V _S)		– 0.3	22	V
Analog Inputs, V _{IN+} , V _{IN–} ⁽²⁾	Differential (V _{IN+}) – (V _{IN–})	– 30	30	V
	Common - mode	– 20	122	V
Output		GND – 0.3	V _S + 0.3	V
T _A	Operating Temperature	– 55	150	°C
T _J	Junction temperature		150	°C
T _{stg}	Storage temperature	– 65	150	°C

(1) Stresses beyond those listed under *Absolute Maximum Rating* may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Condition*. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) V_{IN+} and V_{IN–} are the voltages at the V_{IN+} and V_{IN–} pins, respectively.

7.2 ESD Ratings

			VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins ⁽¹⁾	±2000	V
		Charged device model (CDM), per JEDEC specification JESD22-C101, all pins ⁽²⁾	±1000	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{CM}	Common-mode input range ⁽¹⁾	V _S	48	120	V
V _S	Operating supply range	2.7	5	20	V
T _A	Ambient temperature	– 40		125	°C

(1) Common-mode voltage can go below V_S under certain conditions. See [Figure 8-1](#) for additional information on operating range.

7.4 Thermal Information

THERMAL METRIC ⁽¹⁾		INA2290	INA290	UNIT
		DGK (VSSOP)	DCK (SC-70)	
		8 PINS	5 PINS	
R _{θJA}	Junction-to-ambient thermal resistance	169.3	191.6	°C/W
R _{θJC(top)}	Junction-to-case (top) thermal resistance	60.1	144.4	°C/W
R _{θJB}	Junction-to-board thermal resistance	91.3	69.2	°C/W
Ψ _{JT}	Junction-to-top characterization parameter	8.3	46.2	°C/W
Ψ _{JB}	Junction-to-board characterization parameter	89.7	69.0	°C/W
R _{θJC(bot)}	Junction-to-case (bottom) thermal resistance	N/A	N/A	°C/W

(1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

7.5 Electrical Characteristics

at $T_A = 25\text{ }^{\circ}\text{C}$, $V_S = 5\text{ V}$, $V_{\text{SENSE}} = V_{\text{IN}+} - V_{\text{IN}-} = 0.5\text{ V}$ / Gain, $V_{\text{CM}} = V_{\text{IN}-} = 48\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
INPUT						
CMRR	Common-mode rejection ratio	V _{CM} = 2.7 V to 120 V, T _A = - 40 °C to +125 °C	140	160		dB
		f = 50 kHz		85		
V _{os}	Offset voltage, input referred	A1 devices		5	±25	μV
		A2 devices		3	±20	
		A3 devices		3	±15	
		A4, A5 devices		2	±12	
dV _{os} /dT	Offset voltage drift	T _A = - 40 °C to +125 °C			0.2	μV/°C
PSRR	Power supply rejection ratio, input referred	V _S = 2.7 V to 20 V, T _A = - 40 °C to +125 °C		0.05	±0.5	μV/V
I _B	Input bias current	I _{B+} , V _{SENSE} = 0 mV	10	20	30	μA
		I _{B-} , V _{SENSE} = 0 mV	10	20	30	
OUTPUT						
G	Gain	A1 devices		20		V/V
		A2 devices		50		
		A3 devices		100		
		A4 devices		200		
		A5 devices		500		
	Gain error	A1, A2, A3 devices, GND + 50 mV ≤ V _{OUT} ≤ V _S - 200 mV		0.02	±0.1	%
		A4, A5 devices, GND + 50 mV ≤ V _{OUT} ≤ V _S - 200 mV		0.02	±0.15	
	Gain error drift	T _A = - 40 °C to +125 °C		1.5	5	ppm/°C
	Nonlinearity error			0.01		%
	Maximum capacitive load	No sustained oscillations, no isolation resistor		500		pF
VOLTAGE OUTPUT						
	Swing to V _S power supply rail	R _{LOAD} = 10 k Ω , T _A = - 40 °C to +125 °C		V _S - 0.07	V _S - 0.2	V
	Swing to ground	R _{LOAD} = 10 k Ω , V _{SENSE} = 0 V, T _A = - 40 °C to +125 °C		0.005	0.025	V
FREQUENCY RESPONSE						
BW	Bandwidth	A1 devices, C _{LOAD} = 5 pF, V _{SENSE} = 200 mV		1100		kHz
		A2 devices, C _{LOAD} = 5 pF, V _{SENSE} = 80 mV		1100		
		A3 devices, C _{LOAD} = 5 pF, V _{SENSE} = 40 mV		900		
		A4 devices, C _{LOAD} = 5 pF, V _{SENSE} = 20 mV		850		
		A5 devices, C _{LOAD} = 5 pF, V _{SENSE} = 8 mV		800		
SR	Slew rate			2		V/μs
	Settling time	V _{OUT} =4 V ± 0.1 V step, output settles to 0.5%		9		μs
		V _{OUT} =4 V ± 0.1 V step, output settles to 1%		5		
NOISE						
Ve _n	Voltage noise density			50		nV/ √ Hz
POWER SUPPLY						
V _S	Supply voltage	T _A = - 40 °C to +125 °C	2.7		20	V
I _Q	Quiescent current, INA290			370	500	μA
		T _A = - 40 °C to +125 °C			600	

at $T_A = 25\text{ }^{\circ}\text{C}$, $V_S = 5\text{ V}$, $V_{\text{SENSE}} = V_{\text{IN}+} - V_{\text{IN}-} = 0.5\text{ V} / \text{Gain}$, $V_{\text{CM}} = V_{\text{IN}-} = 48\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
I_Q	Quiescent current, INA2290			700	1000	μA
		$T_A = -40\text{ }^{\circ}\text{C}$ to $+125\text{ }^{\circ}\text{C}$			1200	

7.6 Typical Characteristics

All specifications at $T_A = 25\text{ }^{\circ}\text{C}$, $V_S = 5\text{ V}$, $V_{\text{SENSE}} = V_{\text{IN}+} - V_{\text{IN}-} = 0.5\text{ V} / \text{Gain}$, and $V_{\text{CM}} = V_{\text{IN}-} = 48\text{ V}$, unless otherwise noted.

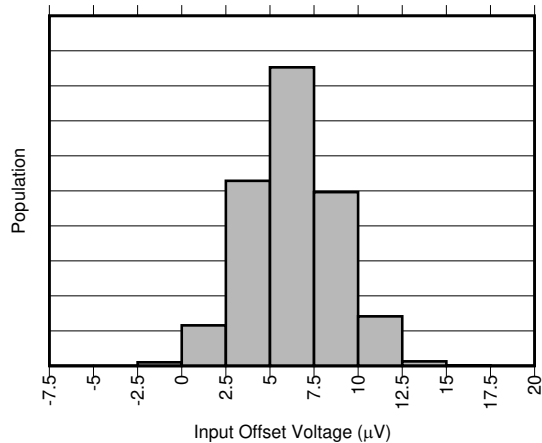


图 7-1. Input Offset Production Distribution, A1 Devices

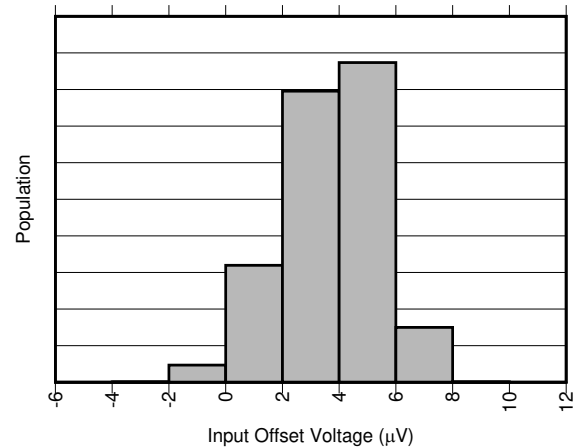


图 7-2. Input Offset Production Distribution, A2 Devices

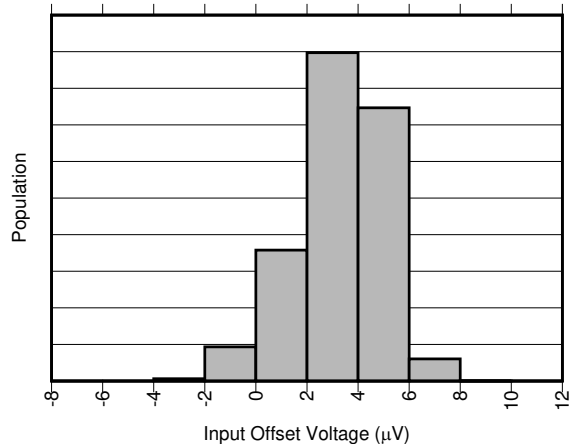


图 7-3. Input Offset Production Distribution, A3 Devices

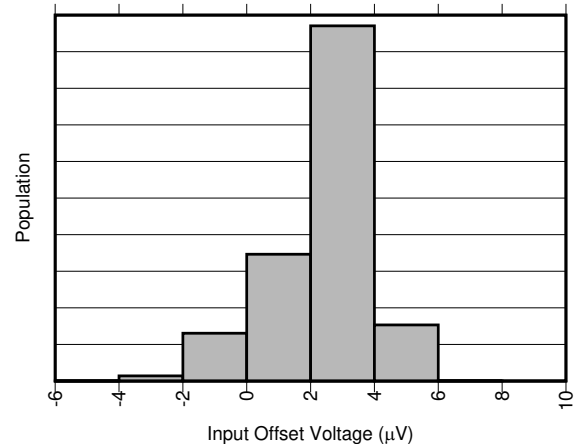


图 7-4. Input Offset Production Distribution, A4 Devices

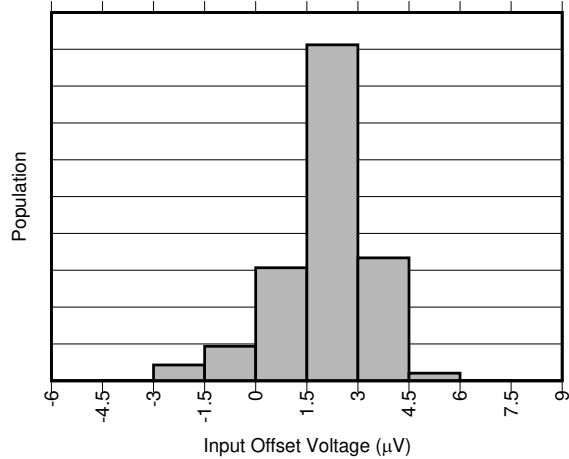


图 7-5. Input Offset Production Distribution, A5 Devices

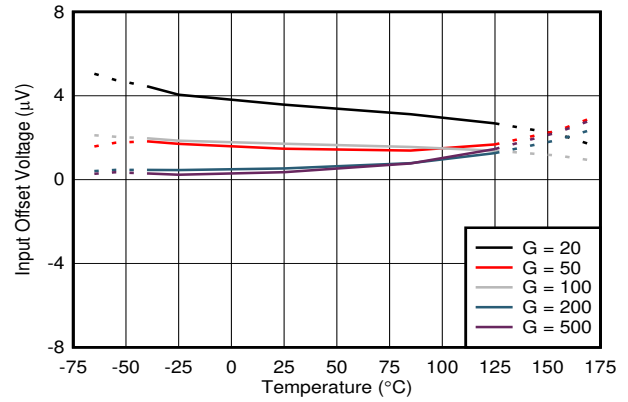


图 7-6. Input Offset Voltage vs Temperature

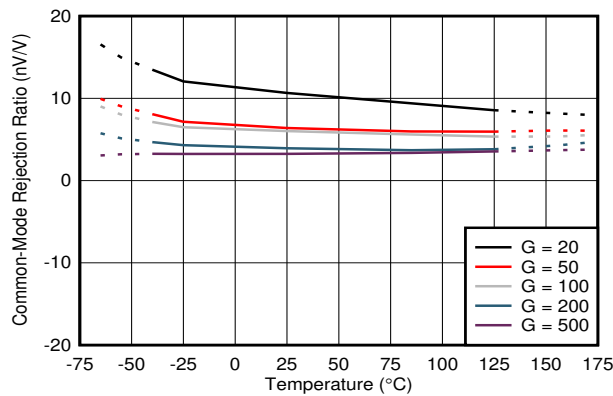


图 7-7. Common-Mode Rejection Ratio vs Temperature

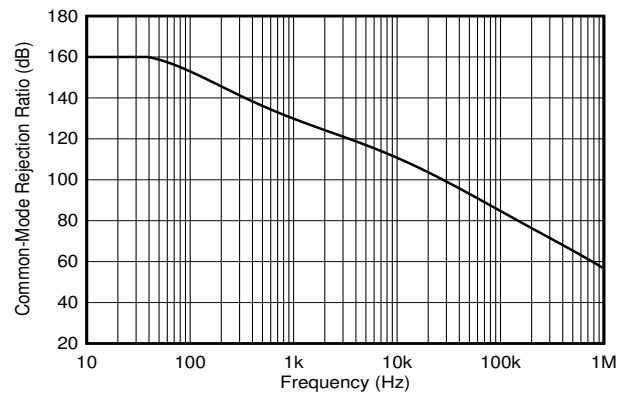


图 7-8. Common-Mode Rejection Ratio vs Frequency

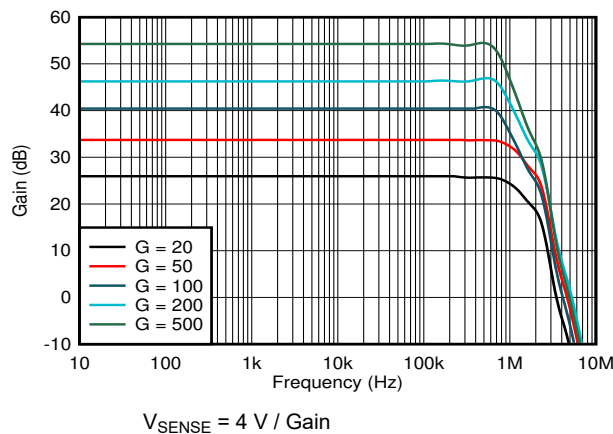


图 7-9. Gain vs Frequency

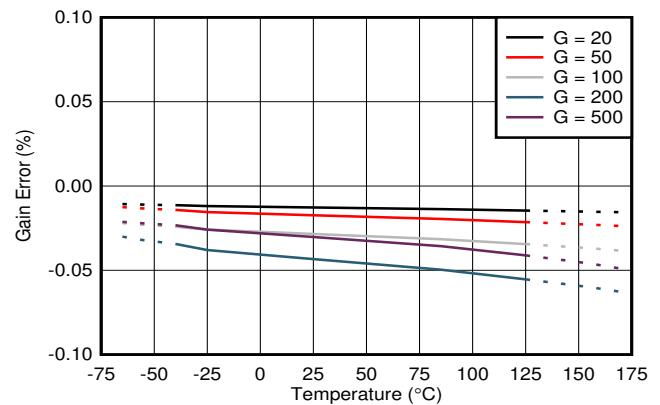


图 7-10. Gain Error vs Temperature

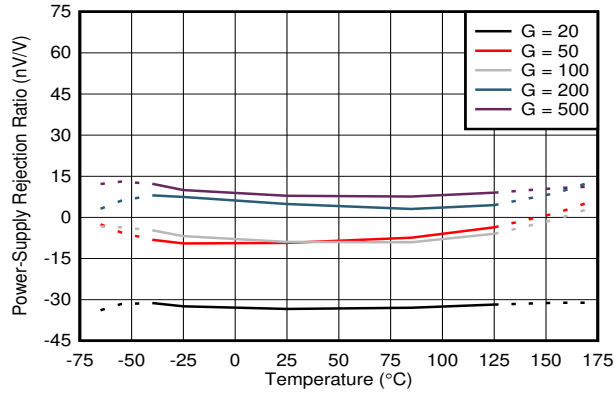


图 7-11. Power-Supply Rejection Ratio vs Temperature

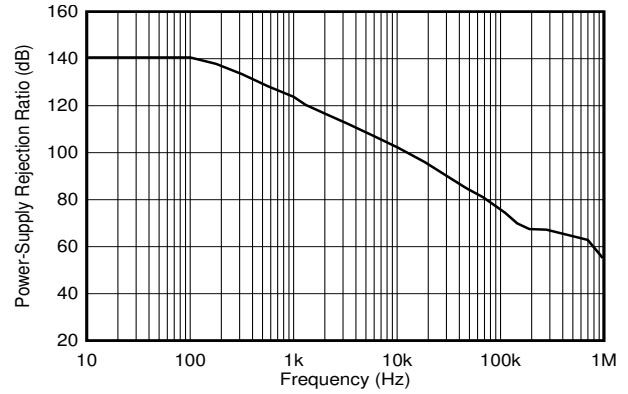


图 7-12. Power-Supply Rejection Ratio vs Frequency

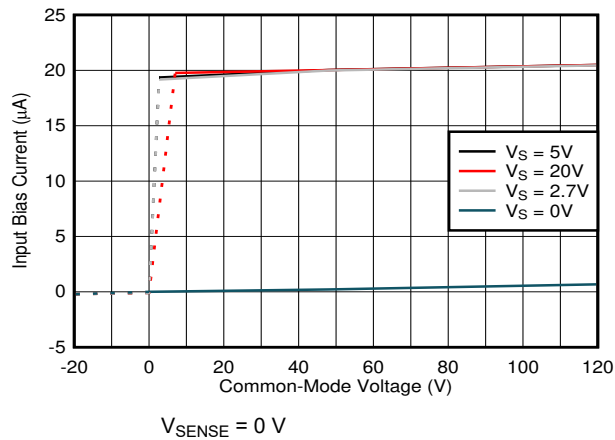


图 7-13. Input Bias Current vs Common-Mode Voltage

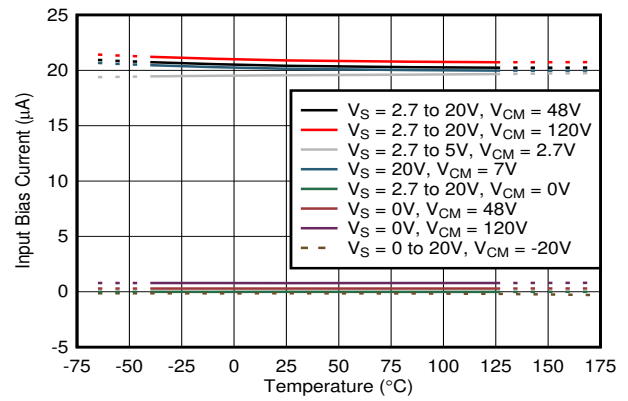


图 7-14. Input Bias Current vs Temperature

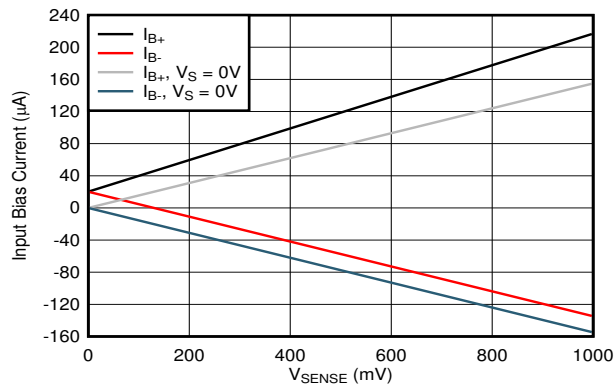


图 7-15. Input Bias Current vs V_{SENSE} , A1 Devices

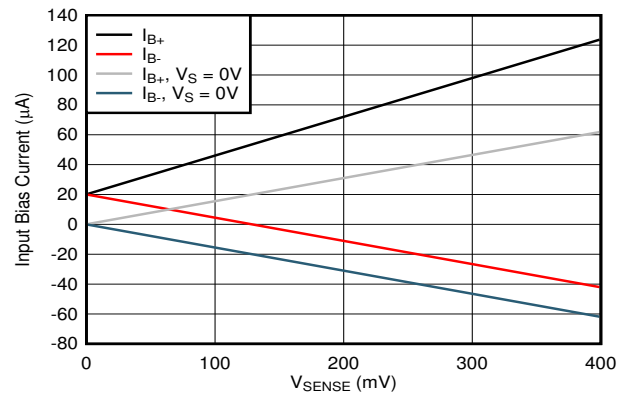


图 7-16. Input Bias Current vs V_{SENSE} , A2 and A3 Devices

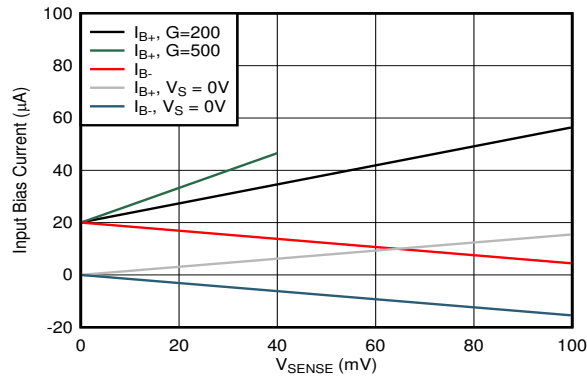


图 7-17. Input Bias Current vs V_{SENSE} , A4 and A5 Devices

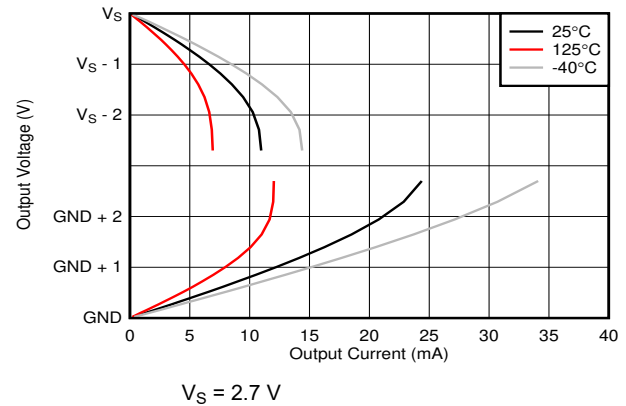


图 7-18. Output Voltage vs Output Current

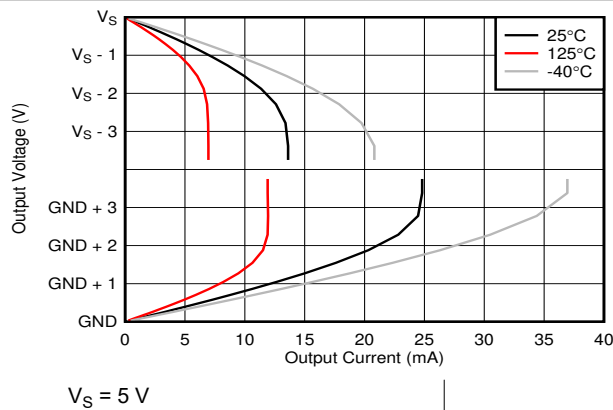


图 7-19. Output Voltage vs Output Current

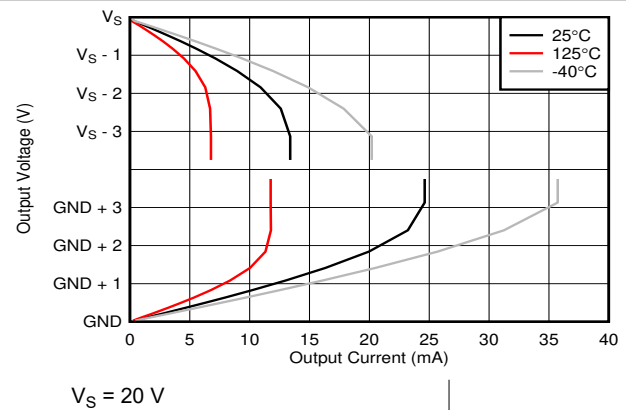


图 7-20. Output Voltage vs Output Current

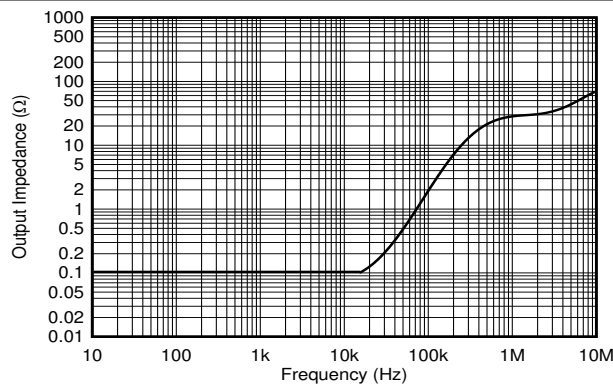


图 7-21. Output Impedance vs Frequency

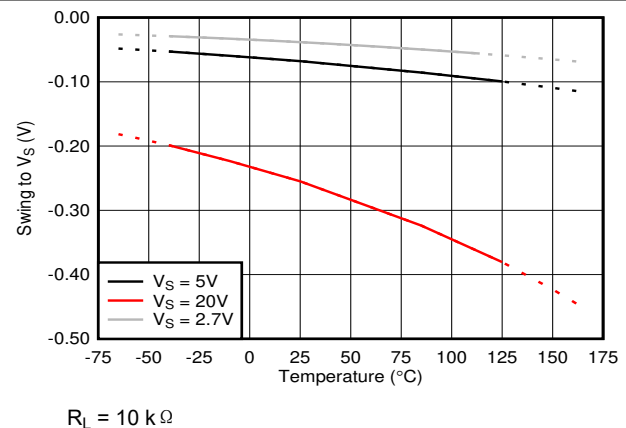


图 7-22. Swing to Supply vs Temperature

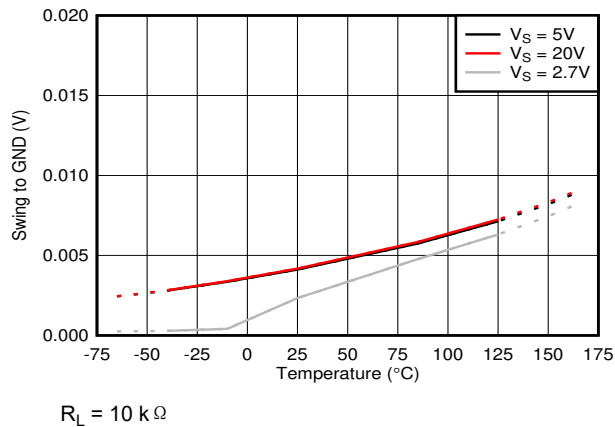


图 7-23. Swing to GND vs Temperature

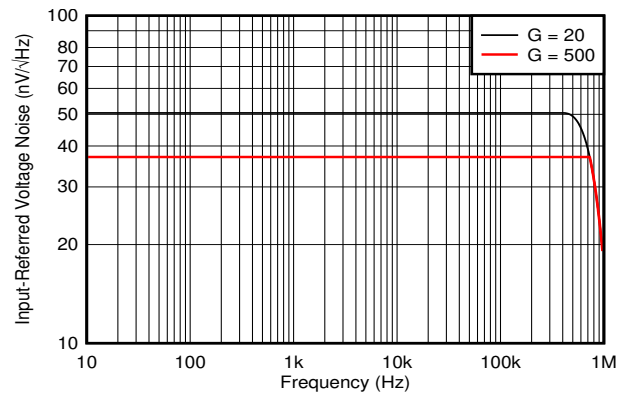


图 7-24. Input Referred Noise vs Frequency

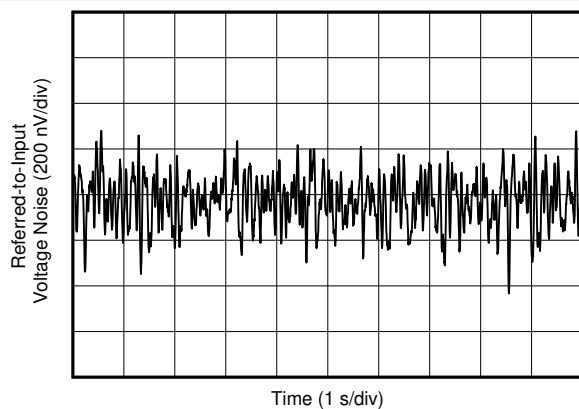


图 7-25. Input Referred Noise

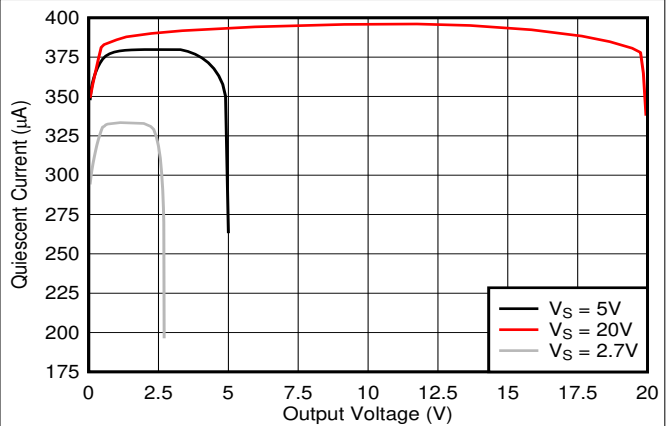


图 7-26. Quiescent Current vs Output Voltage, INA290

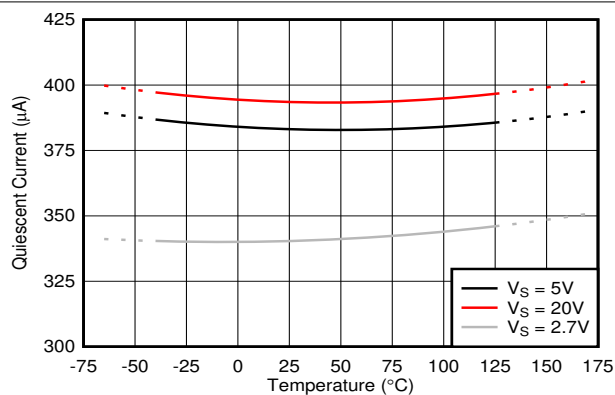


图 7-27. Quiescent Current vs Temperature, INA290

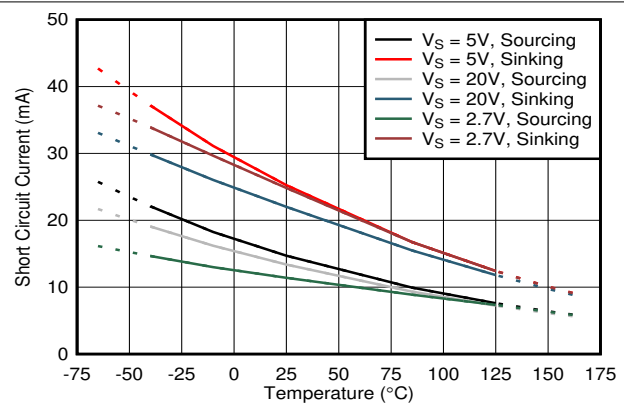


图 7-28. Short-Circuit Current vs Temperature

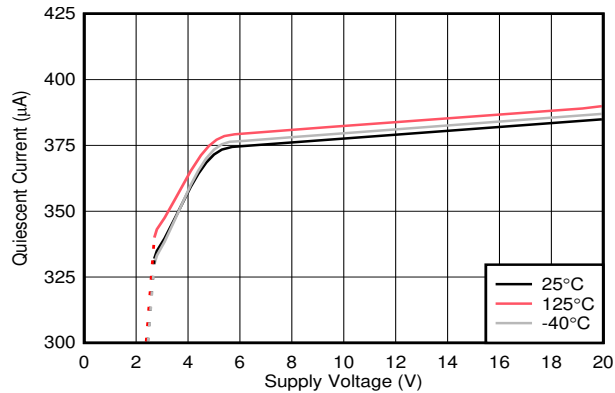


图 7-29. Quiescent Current vs Supply Voltage, INA290

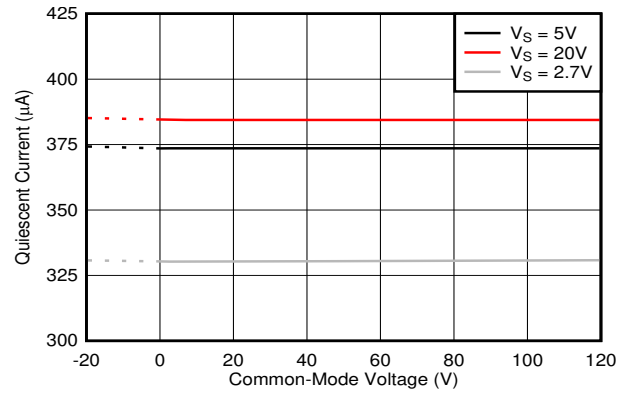


图 7-30. Quiescent Current vs Common-Mode Voltage, INA290

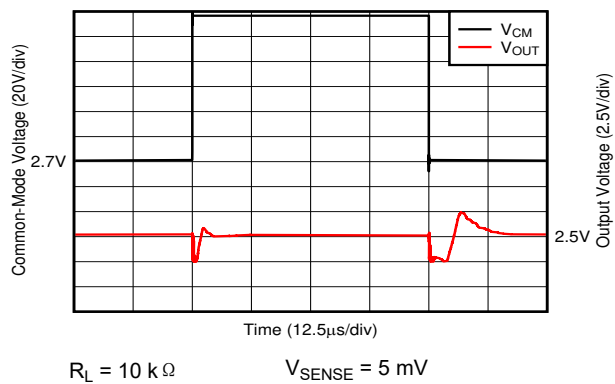


图 7-31. Common-Mode Voltage Fast Transient Pulse, A5 DeviceAs

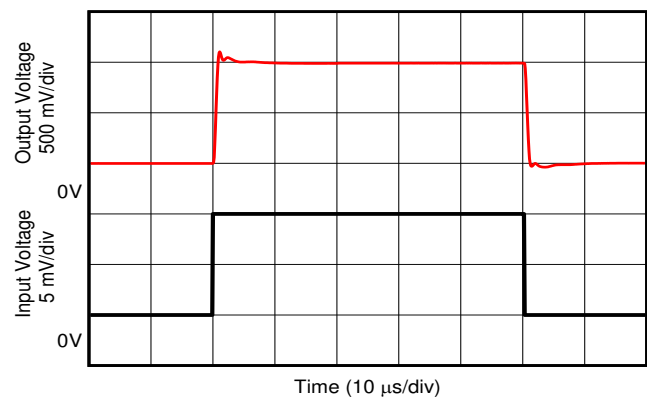


图 7-32. Step Response, A3 Devices

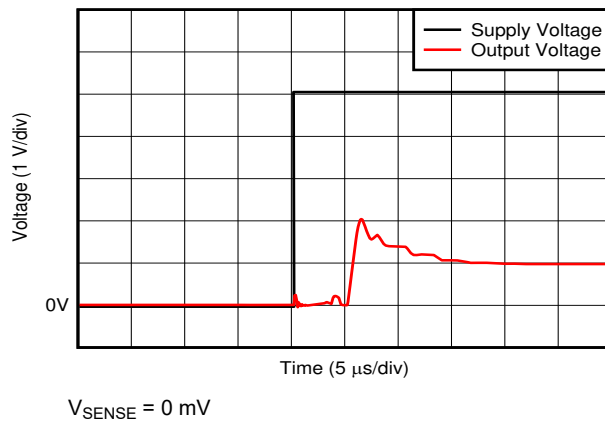


图 7-33. Start-Up Response

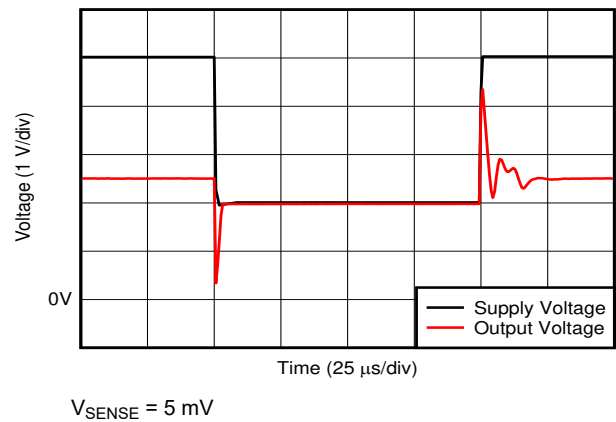


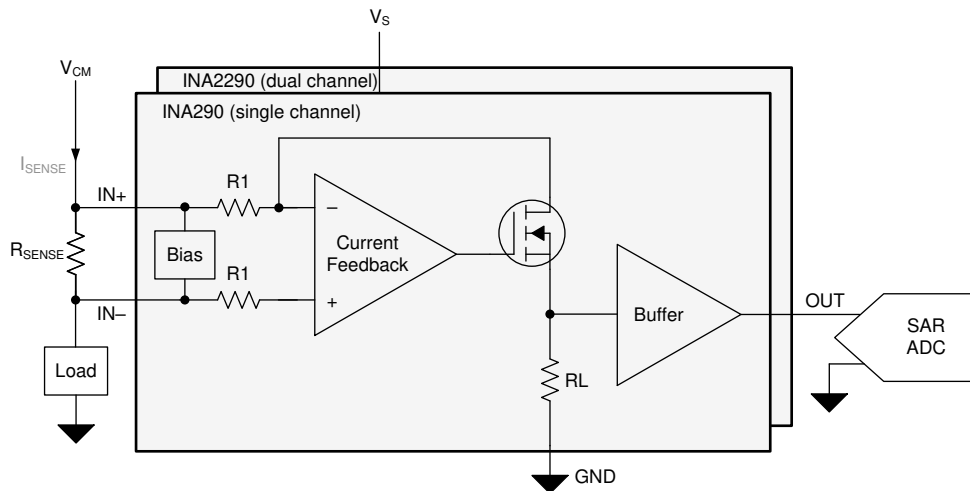
图 7-34. Supply Transient Response, A5 Devices

8 Detailed Description

8.1 Overview

The INAx290 is a high-side only current-sense amplifier that offers a wide common-mode range, precision zero-drift topology, excellent common-mode rejection ratio (CMRR), high bandwidth, and fast slew rate. Different gain versions are available to optimize the output dynamic range based on the application. The INAx290 is designed using a transconductance architecture with a current-feedback amplifier that enables low bias currents of 20 μA and a common-mode voltage of 120 V.

8.2 Functional Block Diagram



8.3 Feature Description

8.3.1 Amplifier Input Common-Mode Range

The INAx290 supports large input common-mode voltages from 2.7 V to 120 V and features a high DC CMRR of 160 dB (typical) and a 85-dB AC CMRR at 50 kHz. The minimum common-mode voltage is restricted by the supply voltage as shown in 图 8-1. The topology of the internal amplifiers INAx290 restricts operation to high-side, current-sensing applications.

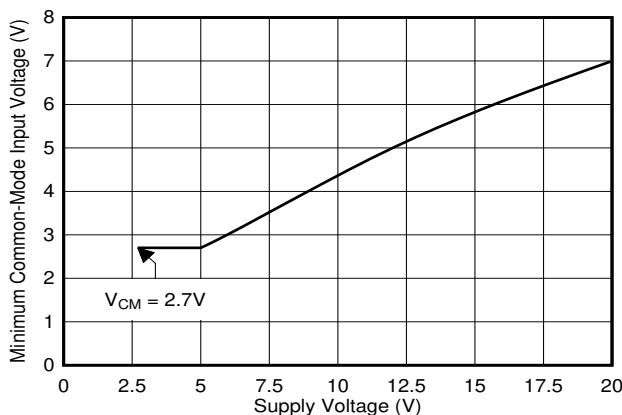


图 8-1. Minimum Common-Mode Voltage vs Supply

8.3.1.1 Input-Signal Bandwidth

The INAx290 -3-dB bandwidth is gain dependent with several gain options of 20 V/V, 50 V/V, 100 V/V, 200 V/V, and 500 V/V as shown in 图 7-8. The unique multistage design enables the amplifier to achieve high bandwidth at all gains. This high bandwidth provides the throughput and fast response that is required for the rapid detection and processing of overcurrent events.

The bandwidth of the device also depends on the applied V_{SENSE} voltage. 图 8-2 shows the bandwidth performance profile of the device over frequency as output voltage increases for each gain variation. As shown in 图 8-2, the device exhibits the highest bandwidth with higher V_{SENSE} voltages, and the bandwidth is higher with lower device gain options. Individual requirements determine the acceptable limits of error for high-frequency, current-sensing applications. Testing and evaluation in the end application or circuit is required to determine the acceptance criteria and validate whether or not the performance levels meet the system specifications.

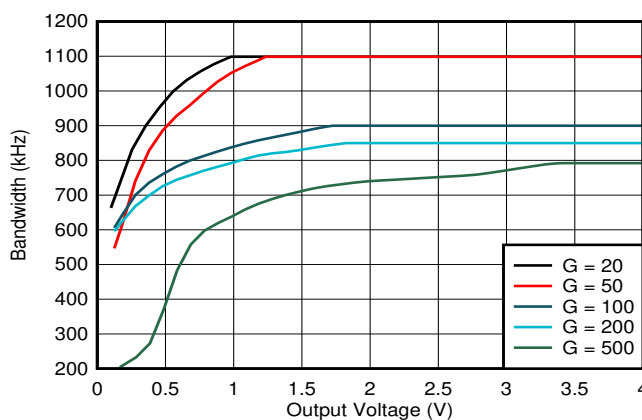


图 8-2. Bandwidth vs Output Voltage

8.3.1.2 Low Input Bias Current

The INAx290 input bias current draws 20 μ A (typical) even with common-mode voltages as high as 120 V. This enables precision current sensing in applications where the sensed current is small or applications that require lower input leakage current.

8.3.1.3 Low V_{SENSE} Operation

The INAx290 enables accurate current measurement across the entire valid V_{SENSE} range. The zero-drift input architecture of the INAx290 provides the low offset voltage and low offset drift needed to measure low V_{SENSE} levels accurately across the wide operating temperature of -40°C to $+125^{\circ}\text{C}$. The capability to measure low sense voltages enables accurate measurements at lower load currents, and also allows reduction of the sense resistor value for a given operating current, which minimizes the power loss in the current sensing element.

8.3.1.4 Wide Fixed Gain Output

The INAx290 gain error is $< 0.1\%$ at room temperature for most gain options, with a maximum drift of 5 ppm/ $^{\circ}\text{C}$ over the full temperature range of -40°C to $+125^{\circ}\text{C}$. The INAx290 is available in multiple gain options of 20 V/V, 50 V/V, 100 V/V, 200 V/V, and 500 V/V, which the system designer should select based on their desired signal-to-noise ratio and other system requirements.

The INAx290 closed-loop gain is set by a precision, low-drift internal resistor network. The ratio of these resistors are excellently matched, while the absolute values may vary significantly. TI does not recommend adding additional resistance around the INAx290 to change the effective gain because of this variation, however. The typical values of the gain resistors are described in 表 8-1.

表 8-1. Fixed Gain Resistor

GAIN	R1	RL
20 (V/V)	25 k Ω	500 k Ω
50 (V/V)	10 k Ω	500 k Ω
100 (V/V)	10 k Ω	1000 k Ω
200 (V/V)	5 k Ω	1000 k Ω
500 (V/V)	2 k Ω	1000 k Ω

8.3.1.5 Wide Supply Range

The INAx290 operates with a wide supply range from a 2.7 V to 20 V. The output stage supports a full-scale output voltage range of up to V_S . Wide output range can enable very-wide dynamic range current measurements. For a gain of 20 V/V, the maximum differential input acceptable is 1 V.

The offset of the gain of INAx290A1 device is $\pm 25 \mu\text{V}$, and the INAx290A1 is capable of measuring a wide dynamic range of current up to 92 dB.

8.4 Device Functional Modes

8.4.1 Unidirectional Operation

The INAx290 measures the differential voltage developed by current flowing through a resistor that is commonly referred to as a current-sensing resistor or a current-shunt resistor. The INAx290 operates in unidirectional mode only, meaning it only senses current sourced from a power supply to a system load as shown in 图 8-3.

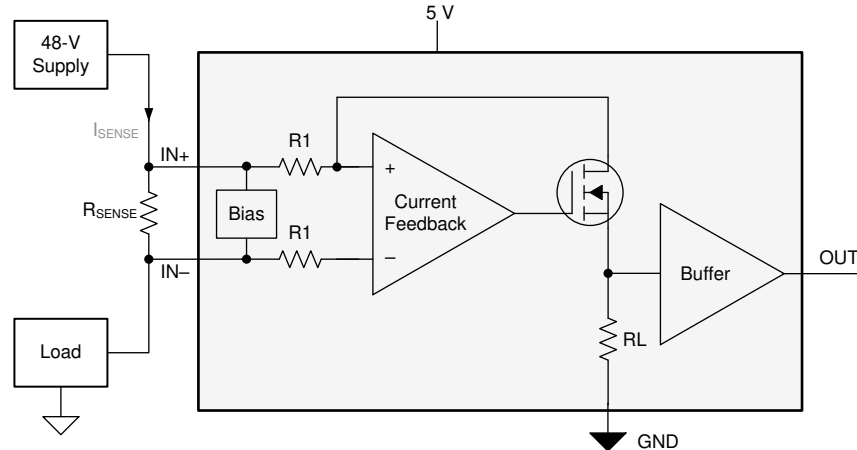


图 8-3. Unidirectional Application (Single Channel Device)

The linear range of the output stage is limited to how close the output voltage can approach ground under zero-input conditions. The zero current output voltage of the INAx290 is very small, with a maximum of GND + 25 mV. Make sure to apply a sense voltage of (25 mV / Gain) or greater to keep the INAx290 output in the linear region of operation.

8.4.2 High Signal Throughput

With a bandwidth of 1.1 MHz at a gain of 20 V/V and a slew rate of 2 V/μs, the INAx290 is specifically designed for detecting and protecting applications from fast inrush currents. As shown in 表 8-2, the INAx290 responds in less than 2 μs for a system measuring a 75-A threshold on a 2-mΩ shunt.

表 8-2. Response Time

PARAMETER		EQUATION	INAx290 AT $V_S = 5\text{ V}$
G	Gain		20 V/V
I_{MAX}	Maximum current		100 A
$I_{Threshold}$	Threshold current		75 A
R_{SENSE}	Current sense resistor value		2 mΩ
V_{OUT_MAX}	Output voltage at maximum current	$V_{OUT} = I_{MAX} \times R_{SENSE} \times G$	4 V
V_{OUT_THR}	Output voltage at threshold current	$V_{OUT_THR} = I_{THR} \times R_{SENSE} \times G$	3 V
SR	Slew rate		2 V/μs
	Output response time	$T_{response} = V_{OUT_THR} / SR$	< 2 μs

9 Application and Implementation

Note

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

The INAx290 amplifies the voltage developed across a current-sensing resistor as current flows through the resistor to the load. The wide input common-mode voltage range and high common-mode rejection of the INAx290 allows use over a wide range of voltage rails while still maintaining an accurate current measurement.

9.1.1 R_{SENSE} and Device Gain Selection

The accuracy of any current-sense amplifier is maximized by choosing the current-sense resistor to be as large as possible. A large sense resistor maximizes the differential input signal for a given amount of current flow and reduces the error contribution of the offset voltage. However, there are practical limits as to how large the current-sense resistor can be in a given application because of the resistor size and maximum allowable power dissipation. [方程式 1](#) gives the maximum value for the current-sense resistor for a given power dissipation budget:

$$R_{\text{SENSE}} < \frac{PD_{\text{MAX}}}{I_{\text{MAX}}^2} \quad (1)$$

where:

- PD_{MAX} is the maximum allowable power dissipation in R_{SENSE} .
- I_{MAX} is the maximum current that will flow through R_{SENSE} .

An additional limitation on the size of the current-sense resistor and device gain is due to the power-supply voltage, V_S , and device swing-to-rail limitations. To make sure that the current-sense signal is properly passed to the output, both positive and negative output swing limitations must be examined. [方程式 2](#) provides the maximum values of R_{SENSE} and GAIN to keep the device from exceeding the positive swing limitation.

$$I_{\text{MAX}} \times R_{\text{SENSE}} \times \text{GAIN} < V_{\text{SP}} \quad (2)$$

where:

- I_{MAX} is the maximum current that will flow through R_{SENSE} .
- GAIN is the gain of the current-sense amplifier.
- V_{SP} is the positive output swing as specified in the data sheet.

To avoid positive output swing limitations when selecting the value of R_{SENSE} , there is always a trade-off between the value of the sense resistor and the gain of the device under consideration. If the sense resistor selected for the maximum power dissipation is too large, then it is possible to select a lower-gain device in order to avoid positive swing limitations.

The negative swing limitation places a limit on how small the sense resistor value can be for a given application. [方程式 3](#) provides the limit on the minimum value of the sense resistor.

$$I_{\text{MIN}} \times R_{\text{SENSE}} \times \text{GAIN} > V_{\text{SN}} \quad (3)$$

where:

- I_{MIN} is the minimum current that will flow through R_{SENSE} .

- GAIN is the gain of the current-sense amplifier.
- V_{SN} is the negative output swing of the device.

表 9-1 shows an example of the different results obtained from using five different gain versions of the INAx290. From the table data, the highest gain device allows a smaller current-shunt resistor and decreased power dissipation in the element.

表 9-1. R_{SENSE} Selection and Power Dissipation

PARAMETER ⁽¹⁾		EQUATION	RESULTS AT $V_S = 5\text{ V}$				
			INAx290A1	INAx290A2	INAx290A3	INAx290A4	INAx290A5
G	Gain		20 V/V	50 V/V	100 V/V	200 V/V	500 V/V
V_{SENSE}	Ideal differential input voltage (Ignores swing limitation and power supply variation.)	$V_{SENSE} = V_{OUT} / G$	250 mV	100 mV	50 mV	25 mV	10 mV
R_{SENSE}	Current sense resistor value	$R_{SENSE} = V_{SENSE} / I_{MAX}$	25 m Ω	10 m Ω	5 m Ω	2.5 m Ω	1 m Ω
P_{SENSE}	Current-sense resistor power dissipation	$R_{SENSE} \times I_{MAX}^2$	2.5 W	1 W	0.5W	0.25 W	0.1 W

(1) Design example with 10-A full-scale current with maximum output voltage set to 5 V.

9.1.2 Input Filtering

Note

Input filters are not required for accurate measurements using the INAx290, and use of filters in this location is not recommended. If filter components are used on the input of the amplifier, follow the guidelines in this section to minimize the effects on performance.

Based strictly on user design requirements, external filtering of the current signal may be desired. The initial location that can be considered for the filter is at the output of the current-sense amplifier. Although placing the filter at the output satisfies the filtering requirements, this location changes the low output impedance measured by any circuitry connected to the output voltage pin. The other location for filter placement is at the current-sense amplifier input pins. This location also satisfies the filtering requirement, but the components must be carefully selected to minimally impact device performance. 图 9-1 shows a filter placed at the input pins.

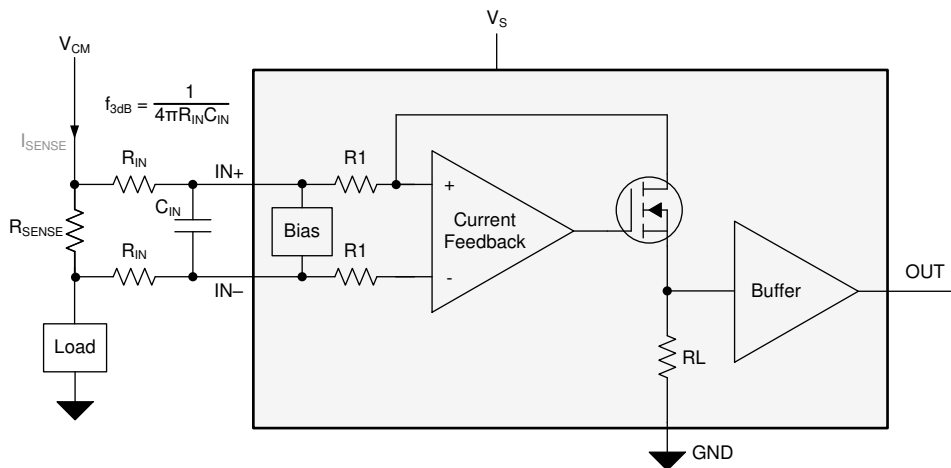


图 9-1. Filter at Input Pins

External series resistance provides a source of additional measurement error, so keep the value of these series resistors to 10 Ω or less to reduce loss of accuracy. The internal bias network shown in 图 9-1 creates a mismatch in input bias currents (see 图 7-15, 图 7-16, and 图 7-17) when a differential voltage is applied between the input pins. If additional external series filter resistors are added to the circuit, a mismatch is created in the voltage drop across the filter resistors. This voltage is a differential error voltage in the shunt resistor voltage. In addition to the absolute resistor value, mismatch resulting from resistor tolerance can significantly impact the error because this value is calculated based on the actual measured resistance.

The measurement error expected from the additional external filter resistors can be calculated using 方程式 4, where the gain error factor is calculated using 方程式 5.

$$\text{Gain Error (\%)} = 100 \times (\text{Gain Error Factor} - 1) \quad (4)$$

The gain error factor, shown in 方程式 4, can be calculated to determine the gain error introduced by the additional external series resistance. 方程式 4 calculates the deviation of the shunt voltage, resulting from the attenuation and imbalance created by the added external filter resistance. 表 9-2 provides the gain error factor and gain error for several resistor values.

$$\text{Gain Error Factor} = \frac{R_B \times R_1}{(R_B \times R_1) + (R_B \times R_{IN}) + (2 \times R_{IN} \times R_1)} \quad (5)$$

Where:

- R_{IN} is the external filter resistance value.
- R_1 is the INAx290 input resistance value specified in 表 8-1.
- R_B in the internal bias resistance, which is $6600 \, \Omega \pm 20\%$.

表 9-2. Example Gain Error Factor and Gain Error for 10- Ω External Filter Input Resistors

DEVICE (GAIN)	GAIN ERROR FACTOR	GAIN ERROR (%)
A1 devices (20)	0.99658	- 0.34185
A2 devices (50)	0.99598	- 0.40141
A3 devices (100)	0.99598	- 0.40141
A4 devices (200)	0.99499	- 0.50051
A5 devices (500)	0.99203	- 0.79663

9.2 Typical Application

The INAx290 is a unidirectional, current-sense amplifier capable of measuring currents through a resistive shunt with shunt common-mode voltages from 2.7 V to 120 V. The circuit configuration for monitoring current in a high-side radio frequency (RF) power amplifier (PA) application is shown in 图 9-2.

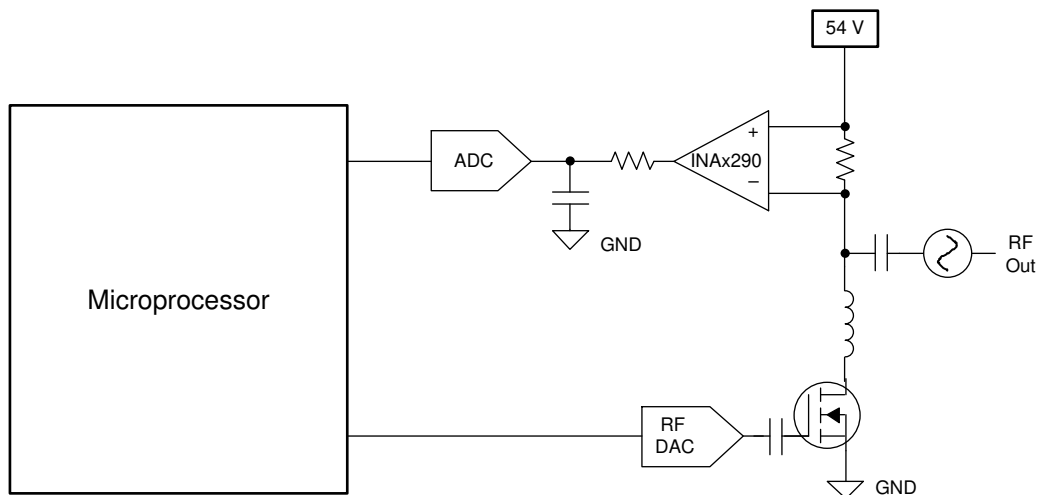


图 9-2. Current Sensing in a PA Application

9.2.1 Design Requirements

V_{SUPPLY} is set to 5 V, and the common-mode voltage set to 54 V. 表 9-3 lists the design setup for this application.

表 9-3. Design Parameters

DESIGN PARAMETERS	EXAMPLE VALUE
INAx290 supply voltage	5 V
High-side supply voltage	5 V
Maximum sense current (I_{MAX})	5 A
Gain option	50 V/V

9.2.2 Detailed Design Procedure

The maximum value of the current-sense resistor is calculated based choice of gain, value of the maximum current the be sensed (I_{MAX}), and the power-supply voltage (V_S). When operating at the maximum current, the output voltage must not exceed the positive output swing specification, V_{SP} . Under the given design parameters, 方程式 6 calculates the maximum value for R_{SENSE} as 19.2 m Ω .

$$R_{SENSE} < \frac{V_{SP}}{I_{MAX} \times GAIN} \quad (6)$$

For this design example, a value of 15 m Ω is selected because, while the 15 m Ω is less than the maximum value calculated, 15 m Ω is still large enough to give adequate signal at the current-sense amplifier output.

9.2.2.1 Overload Recovery With Negative V_{SENSE}

The INAx290 is a unidirectional current-sense amplifier that is meant to operate with a positive differential input voltage (V_{SENSE}). If negative V_{SENSE} is applied, the device is placed in an overload condition and requires time to recover once V_{SENSE} returns positive. The required overload recovery time increases with more negative V_{SENSE} .

9.2.3 Application Curve

图 9-3 shows the output response of the device to a high frequency sinusoidal current.

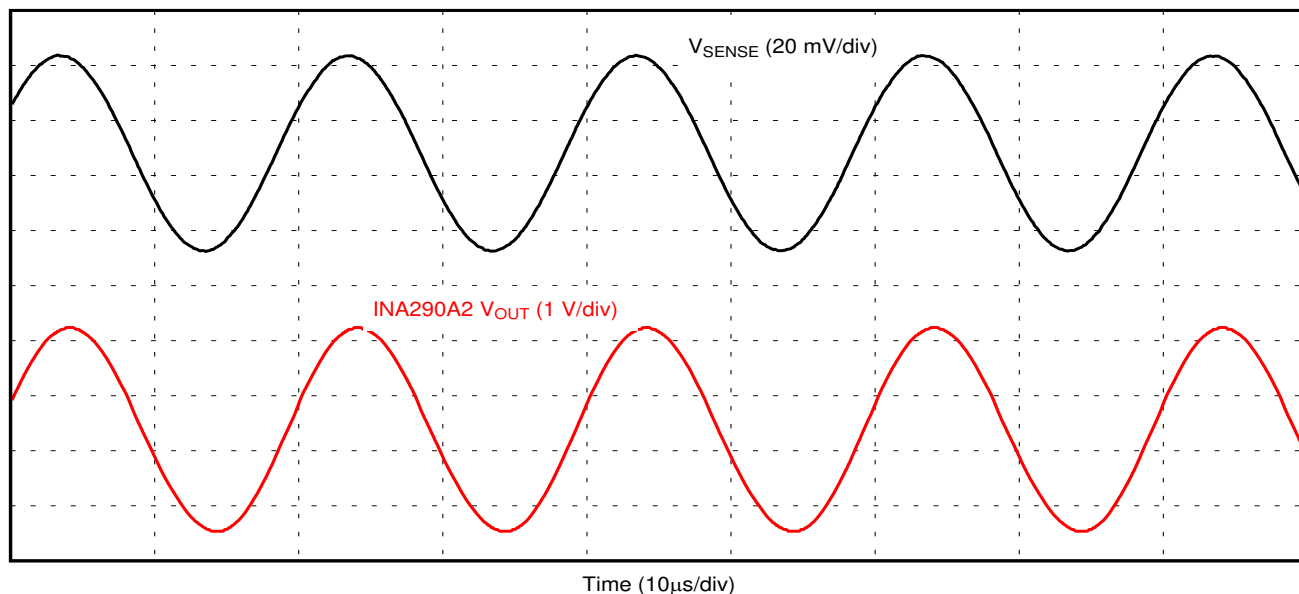


图 9-3. INAx290 Output Response

10 Power Supply Recommendations

The input circuitry of the INAx290 device can accurately measure beyond the power-supply voltage. The power supply can be 20 V, whereas the load power-supply voltage at IN+ and IN – can go up to 120 V. The output voltage range of the OUT pin is limited by the voltage on the V_S pin and the device swing to supply specification.

11 Layout

11.1 Layout Guidelines

TI always recommends to follow good layout practices:

- Connect the input pins to the sensing resistor using a Kelvin or 4-wire connection. This connection technique makes sure that only the current-sensing resistor impedance is detected between the input pins. Poor routing of the current-sensing resistor commonly results in additional resistance present between the input pins. Given the very low ohmic value of the current resistor, any additional high-current carrying impedance can cause significant measurement errors.
- Place the power-supply bypass capacitor as close to the device power supply and ground pins as possible. The recommended value of this bypass capacitor is 0.1 μ F. Additional decoupling capacitance can be added to compensate for noisy or high-impedance power supplies.
- When routing the connections from the current-sense resistor to the device, keep the trace lengths as short as possible.

11.2 Layout Example

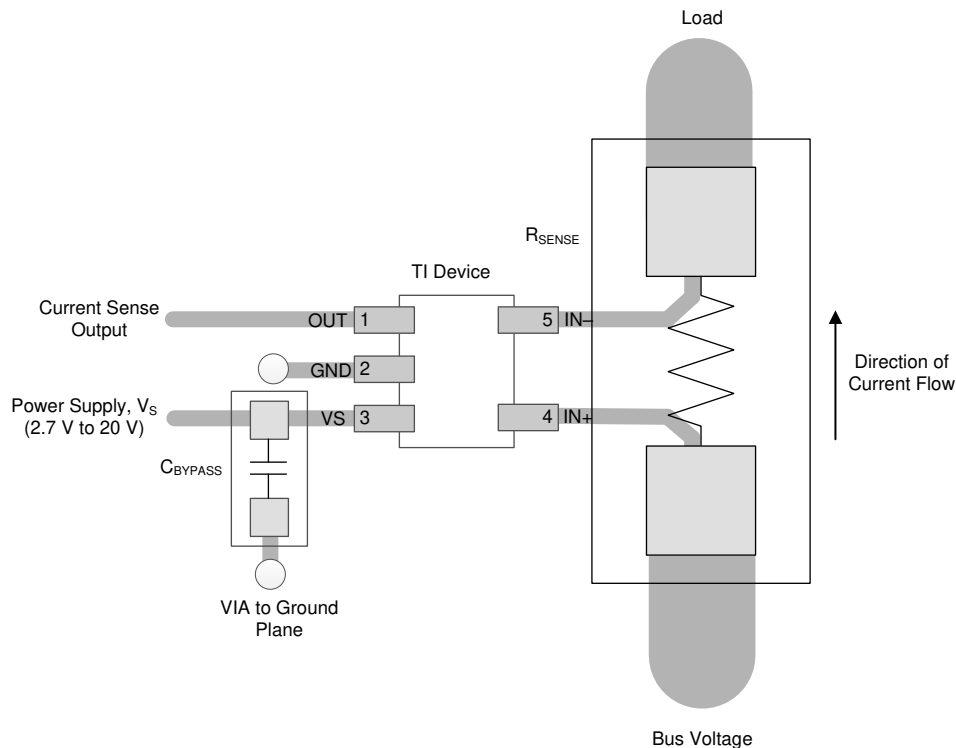


图 11-1. Recommended Layout for INA290

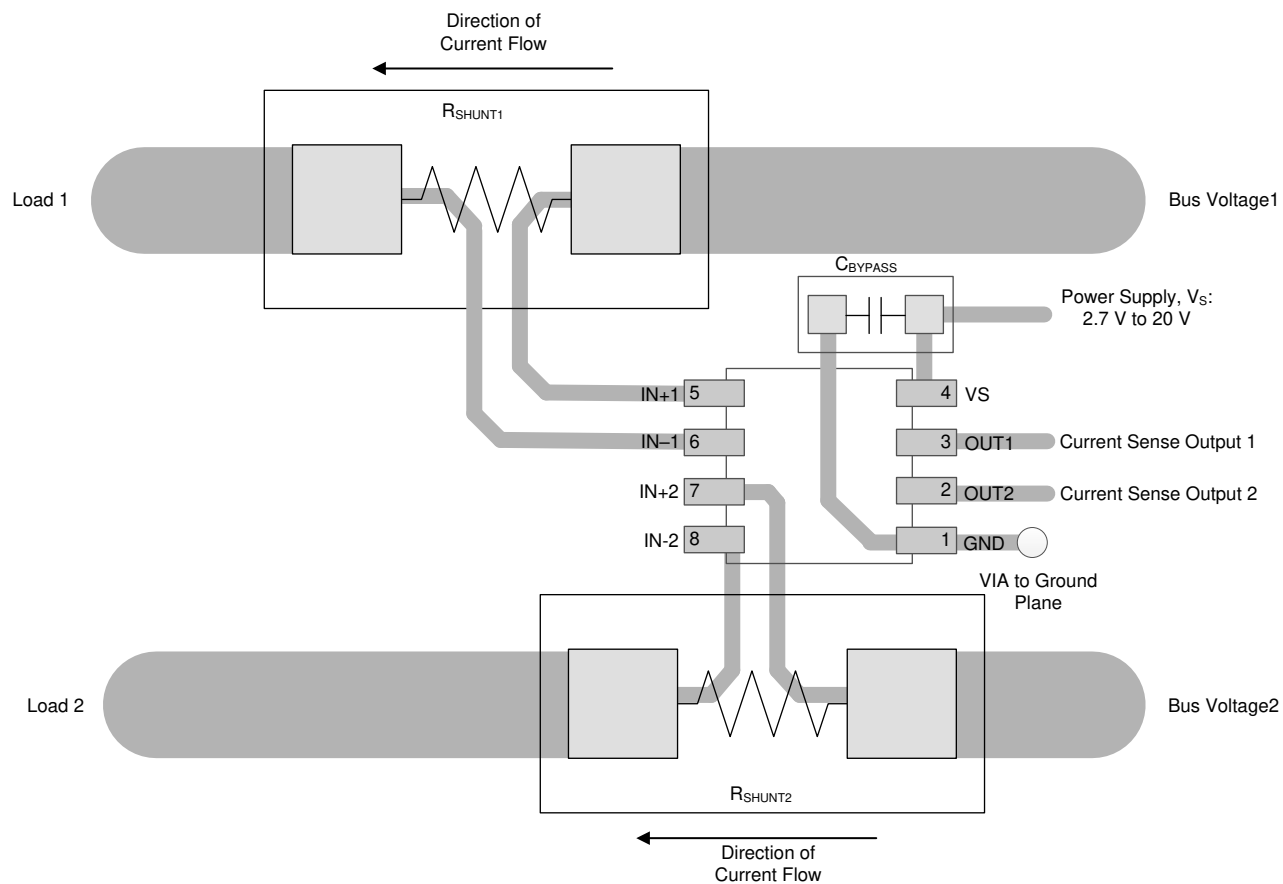


图 11-2. Recommended Layout for INA2290

12 Device and Documentation Support

12.1 Documentation Support

12.1.1 Related Documentation

For related documentation, see the following:

Texas Instruments, [INA290EVM User's Guide](#)

12.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on [ti.com](#). Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

12.3 Support Resources

[TI E2E™ support forums](#) are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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12.4 Trademarks

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12.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

12.6 Glossary

[TI Glossary](#) This glossary lists and explains terms, acronyms, and definitions.

13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

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PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
INA2290A1IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FAQ	Samples
INA2290A1IDGKT	ACTIVE	VSSOP	DGK	8	250	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FAQ	Samples
INA2290A2IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FBQ	Samples
INA2290A2IDGKT	ACTIVE	VSSOP	DGK	8	250	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FBQ	Samples
INA2290A3IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FCQ	Samples
INA2290A3IDGKT	ACTIVE	VSSOP	DGK	8	250	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FCQ	Samples
INA2290A4IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FDQ	Samples
INA2290A4IDGKT	ACTIVE	VSSOP	DGK	8	250	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FDQ	Samples
INA2290A5IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FEQ	Samples
INA2290A5IDGKT	ACTIVE	VSSOP	DGK	8	250	RoHS & Green	NIPDAU	Level-2-260C-1 YEAR	-40 to 125	2FEQ	Samples
INA290A1IDCKR	ACTIVE	SC70	DCK	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FQ	Samples
INA290A1IDCKT	ACTIVE	SC70	DCK	5	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FQ	Samples
INA290A2IDCKR	ACTIVE	SC70	DCK	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FR	Samples
INA290A2IDCKT	ACTIVE	SC70	DCK	5	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FR	Samples
INA290A3IDCKR	ACTIVE	SC70	DCK	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FS	Samples
INA290A3IDCKT	ACTIVE	SC70	DCK	5	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FS	Samples
INA290A4IDCKR	ACTIVE	SC70	DCK	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FT	Samples
INA290A4IDCKT	ACTIVE	SC70	DCK	5	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FT	Samples
INA290A5IDCKR	ACTIVE	SC70	DCK	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FU	Samples
INA290A5IDCKT	ACTIVE	SC70	DCK	5	250	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	1FU	Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of ≤ 1000 ppm threshold. Antimony trioxide based flame retardants must also meet the ≤ 1000 ppm threshold requirement.

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

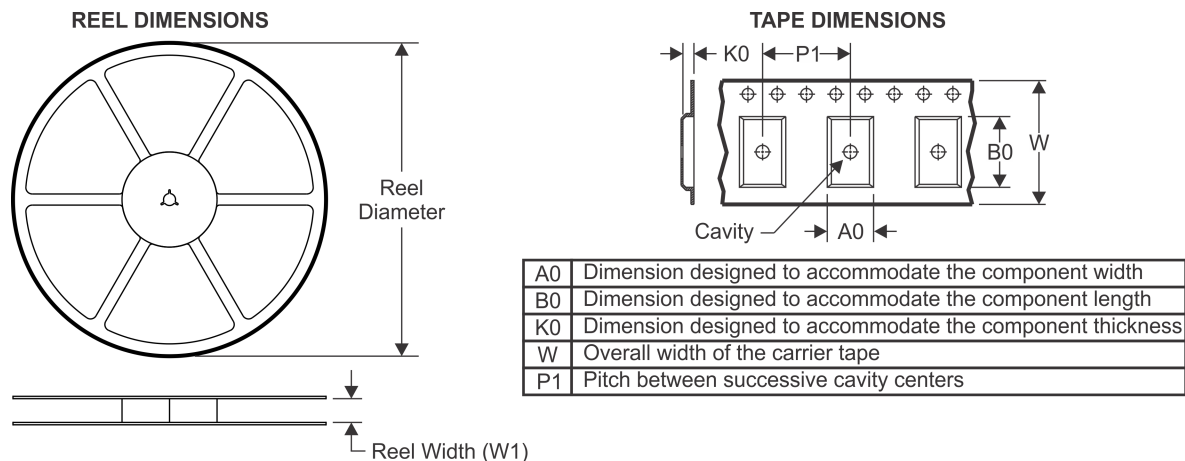
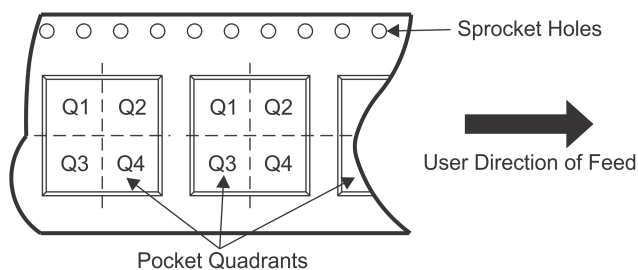
⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

⁽⁶⁾ Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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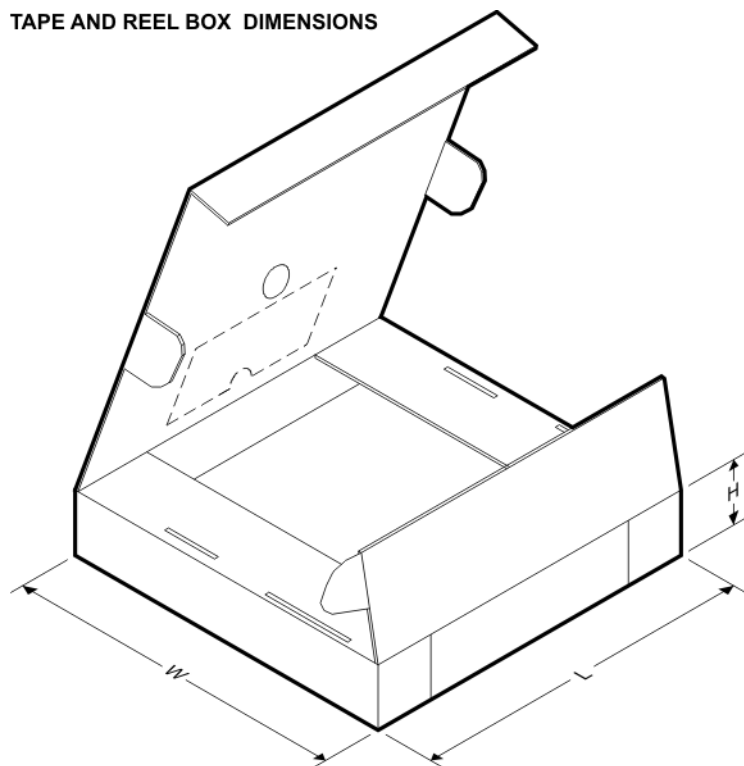
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TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA2290A1IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A1IDGKT	VSSOP	DGK	8	250	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A2IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A2IDGKT	VSSOP	DGK	8	250	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A3IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A3IDGKT	VSSOP	DGK	8	250	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A4IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A4IDGKT	VSSOP	DGK	8	250	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A5IDGKR	VSSOP	DGK	8	2500	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA2290A5IDGKT	VSSOP	DGK	8	250	330.0	12.4	5.3	3.4	1.4	8.0	12.0	Q1
INA290A1IDCKR	SC70	DCK	5	3000	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A1IDCKT	SC70	DCK	5	250	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A2IDCKR	SC70	DCK	5	3000	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A2IDCKT	SC70	DCK	5	250	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A3IDCKR	SC70	DCK	5	3000	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A3IDCKT	SC70	DCK	5	250	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A4IDCKR	SC70	DCK	5	3000	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A4IDCKT	SC70	DCK	5	250	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
INA290A5IDCKR	SC70	DCK	5	3000	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3
INA290A5IDCKT	SC70	DCK	5	250	180.0	8.4	2.47	2.3	1.25	4.0	8.0	Q3

TAPE AND REEL BOX DIMENSIONS


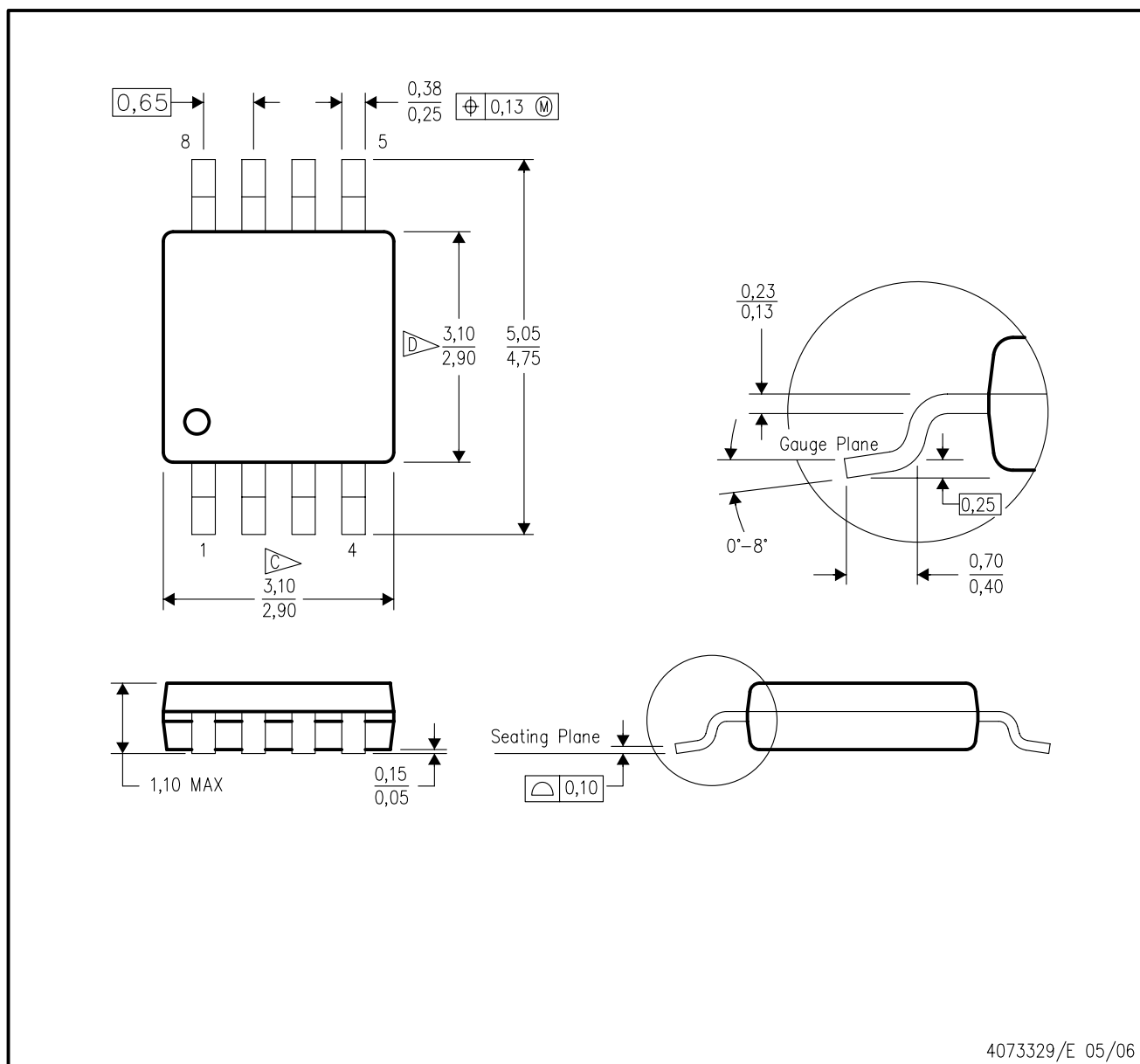
*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA2290A1IDGKR	VSSOP	DGK	8	2500	366.0	364.0	50.0
INA2290A1IDGKT	VSSOP	DGK	8	250	366.0	364.0	50.0
INA2290A2IDGKR	VSSOP	DGK	8	2500	366.0	364.0	50.0
INA2290A2IDGKT	VSSOP	DGK	8	250	366.0	364.0	50.0
INA2290A3IDGKR	VSSOP	DGK	8	2500	366.0	364.0	50.0
INA2290A3IDGKT	VSSOP	DGK	8	250	366.0	364.0	50.0
INA2290A4IDGKR	VSSOP	DGK	8	2500	366.0	364.0	50.0
INA2290A4IDGKT	VSSOP	DGK	8	250	366.0	364.0	50.0
INA2290A5IDGKR	VSSOP	DGK	8	2500	366.0	364.0	50.0
INA2290A5IDGKT	VSSOP	DGK	8	250	366.0	364.0	50.0
INA290A1IDCKR	SC70	DCK	5	3000	183.0	183.0	20.0
INA290A1IDCKT	SC70	DCK	5	250	183.0	183.0	20.0
INA290A2IDCKR	SC70	DCK	5	3000	183.0	183.0	20.0
INA290A2IDCKT	SC70	DCK	5	250	183.0	183.0	20.0
INA290A3IDCKR	SC70	DCK	5	3000	183.0	183.0	20.0

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
INA290A3IDCKT	SC70	DCK	5	250	183.0	183.0	20.0
INA290A4IDCKR	SC70	DCK	5	3000	183.0	183.0	20.0
INA290A4IDCKT	SC70	DCK	5	250	183.0	183.0	20.0
INA290A5IDCKR	SC70	DCK	5	3000	183.0	183.0	20.0
INA290A5IDCKT	SC70	DCK	5	250	183.0	183.0	20.0

DGK (S-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE

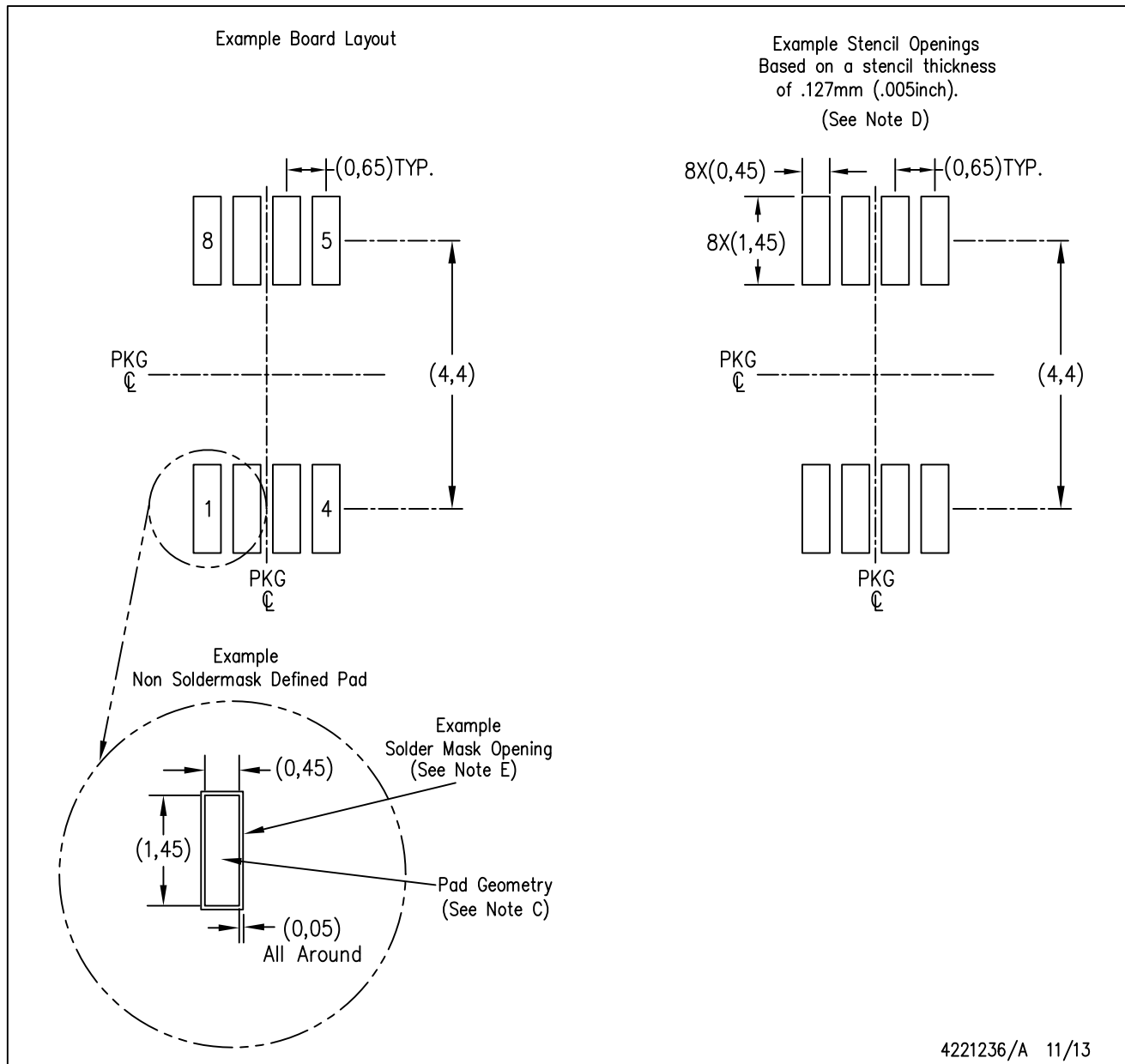


4073329/E 05/06

- NOTES:
- A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 per end.
 - D. Body width does not include interlead flash. Interlead flash shall not exceed 0.50 per side.
 - E. Falls within JEDEC MO-187 variation AA, except interlead flash.

DGK (S-PDSO-G8)

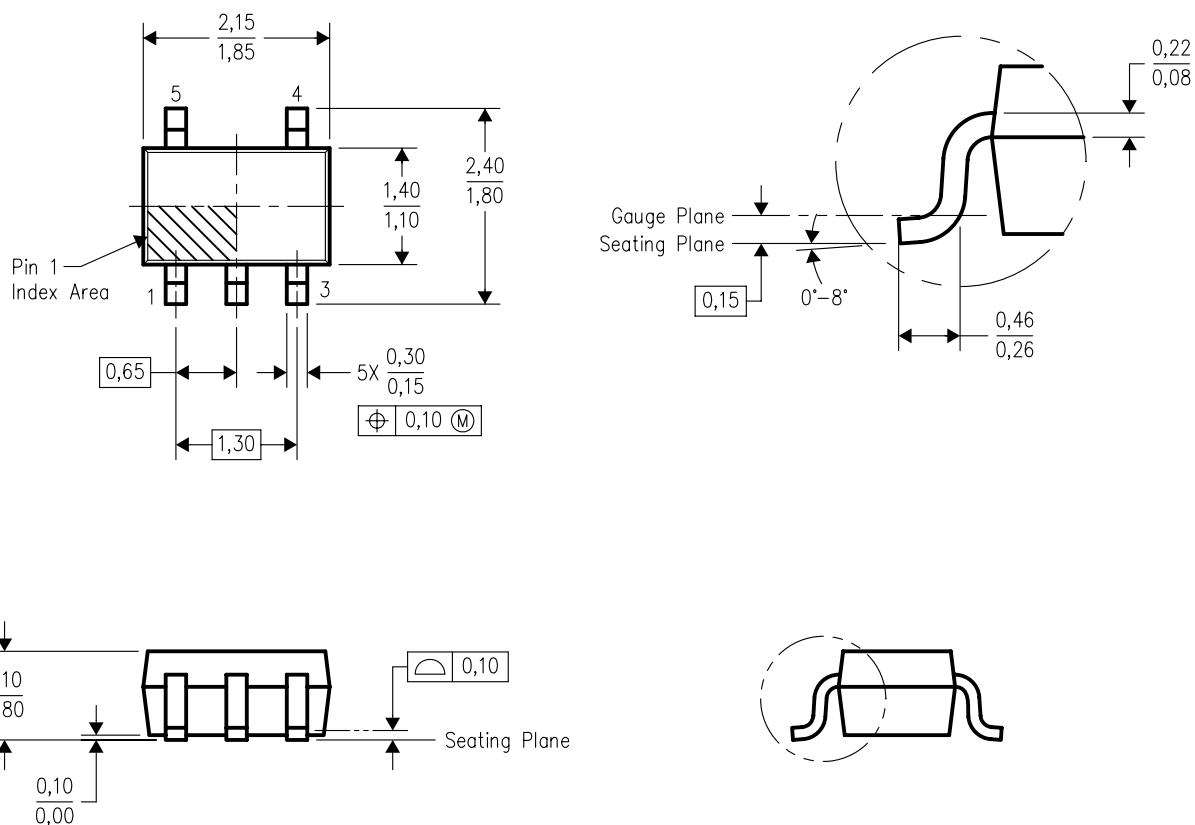
PLASTIC SMALL OUTLINE PACKAGE



- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Publication IPC-7351 is recommended for alternate designs.
 - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
 - Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

DCK (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



4093553-3/G 01/2007

- NOTES:
- All linear dimensions are in millimeters.
 - This drawing is subject to change without notice.
 - Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - Falls within JEDEC MO-203 variation AA.

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